

123456

A

Baseband Schematic

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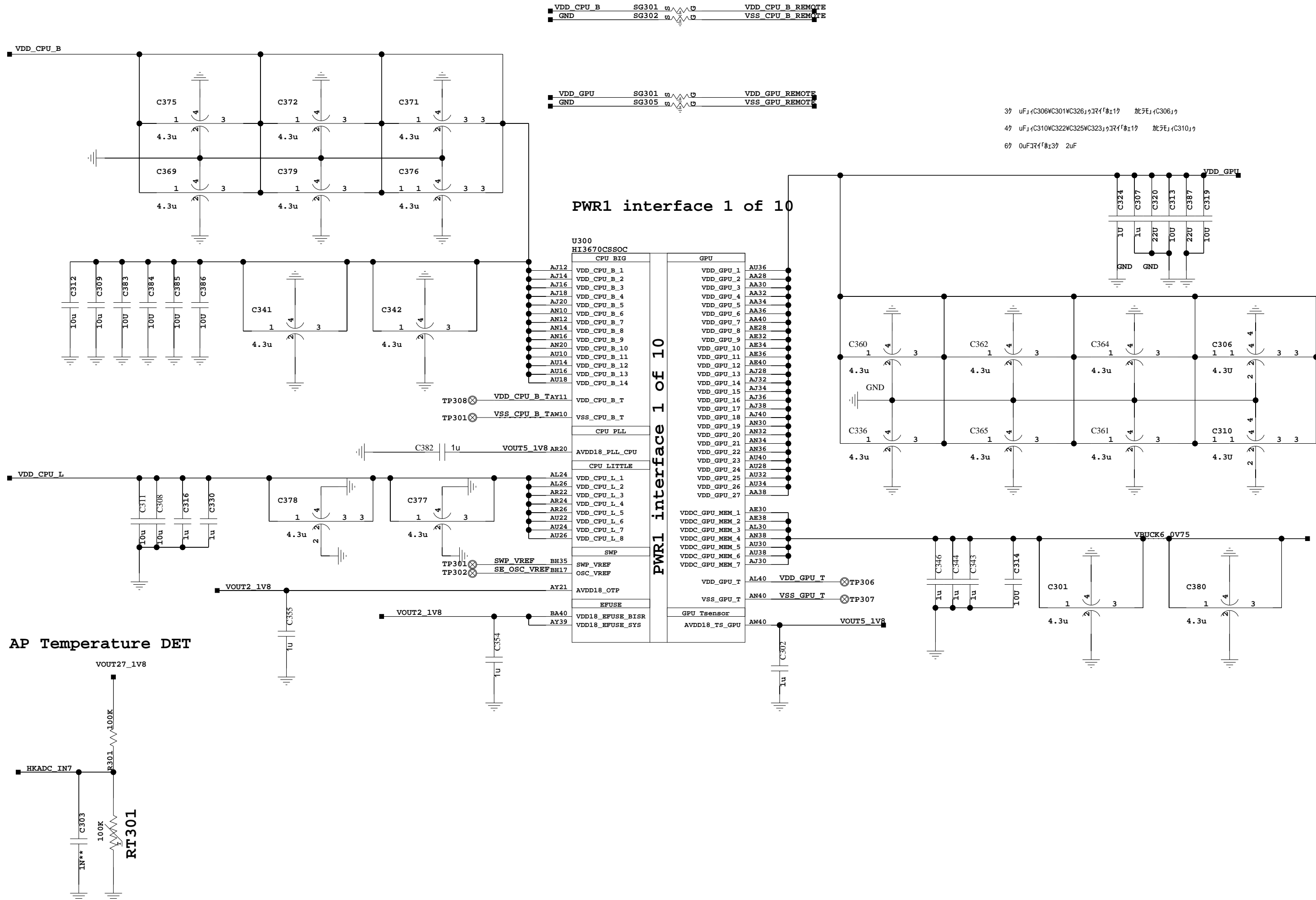
MODEM Schematic

31. RF Interface
32. APT_LM3243
33. RFIC0_Hi6363
34. DPDT_Connector
35. TXM
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44. PRX_LNA_Module
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46. DRX_LB
47. DRX_MB
48. DRX_HB
49. DRX_LNA_Module
50. Reserved
51. Reserved
52. Reserved
53. Reserved
54. Reserved
55. Reserved
56. Reserved
57. Reserved
58. Reserved
59. Reserved
60. NC_POWER
61. NC_BB
62. NC_FE_WIFI_2G
63. NC_FE_WIFI_5G
64. NC_GPS
65. RES
66. NFC Antena
67. RES
68. NFC_BB
69. RES
70. Antenna
71. NFC Antena

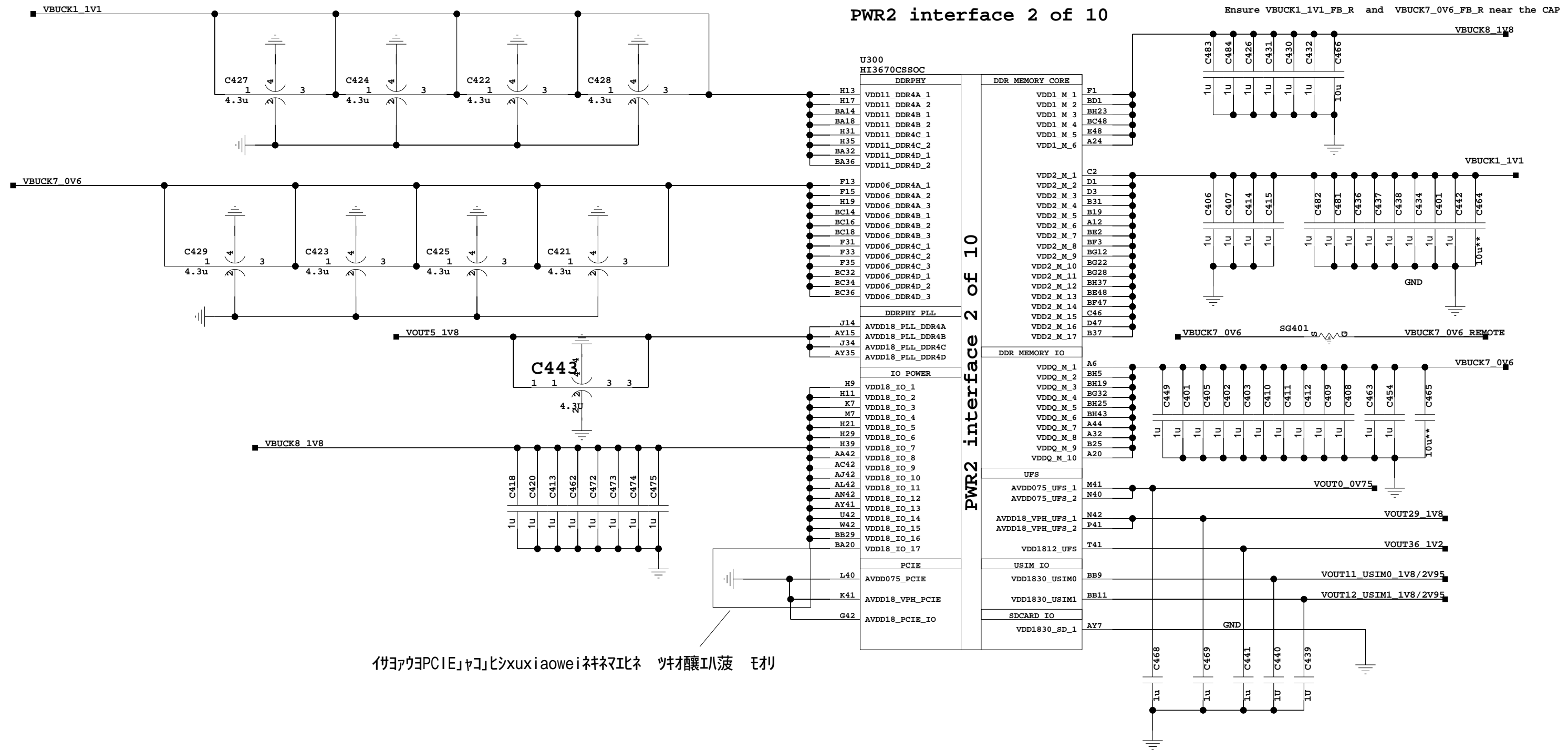
2.RES

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

3.SOC PWR1

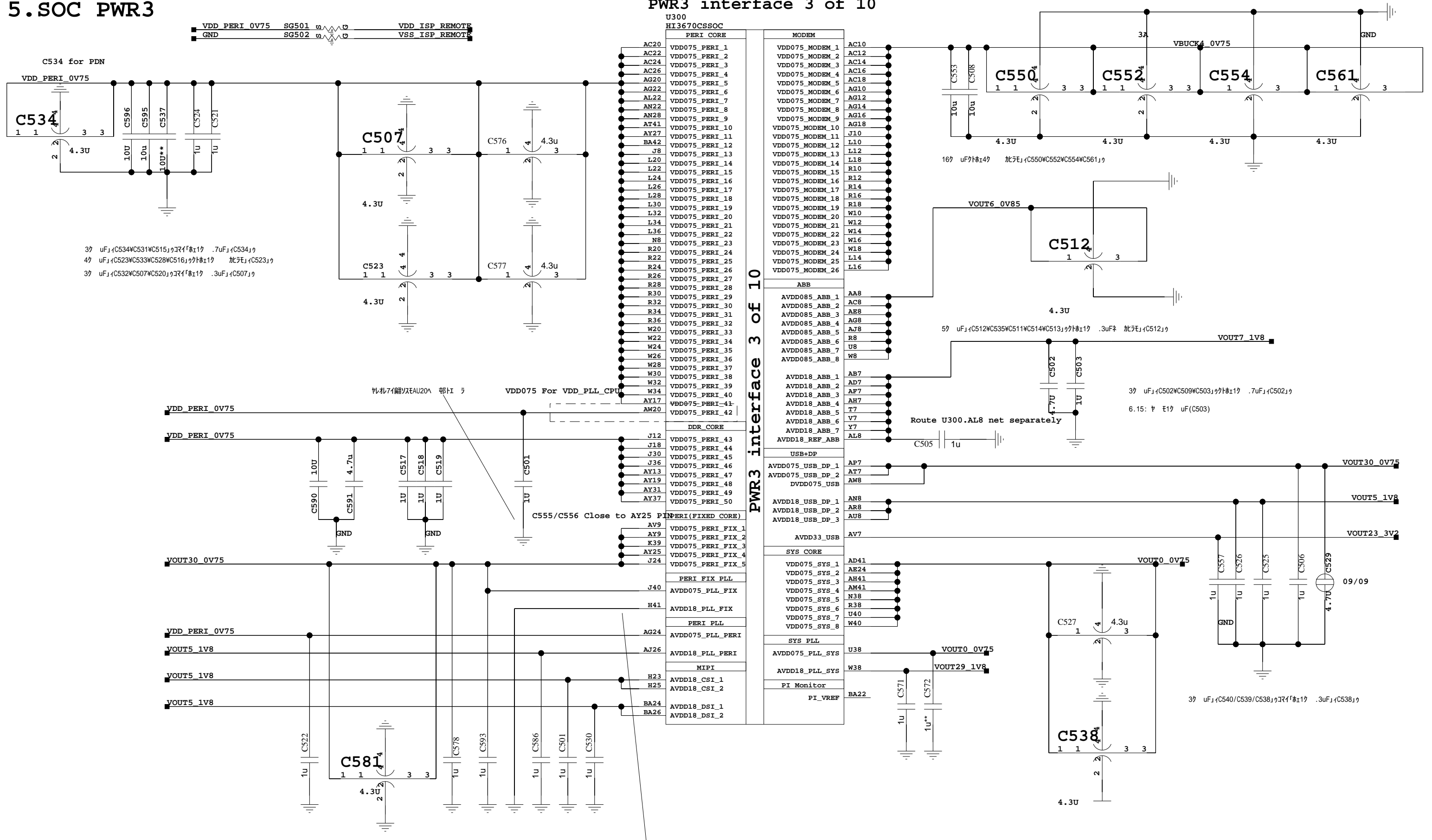


4.SOC PWR2



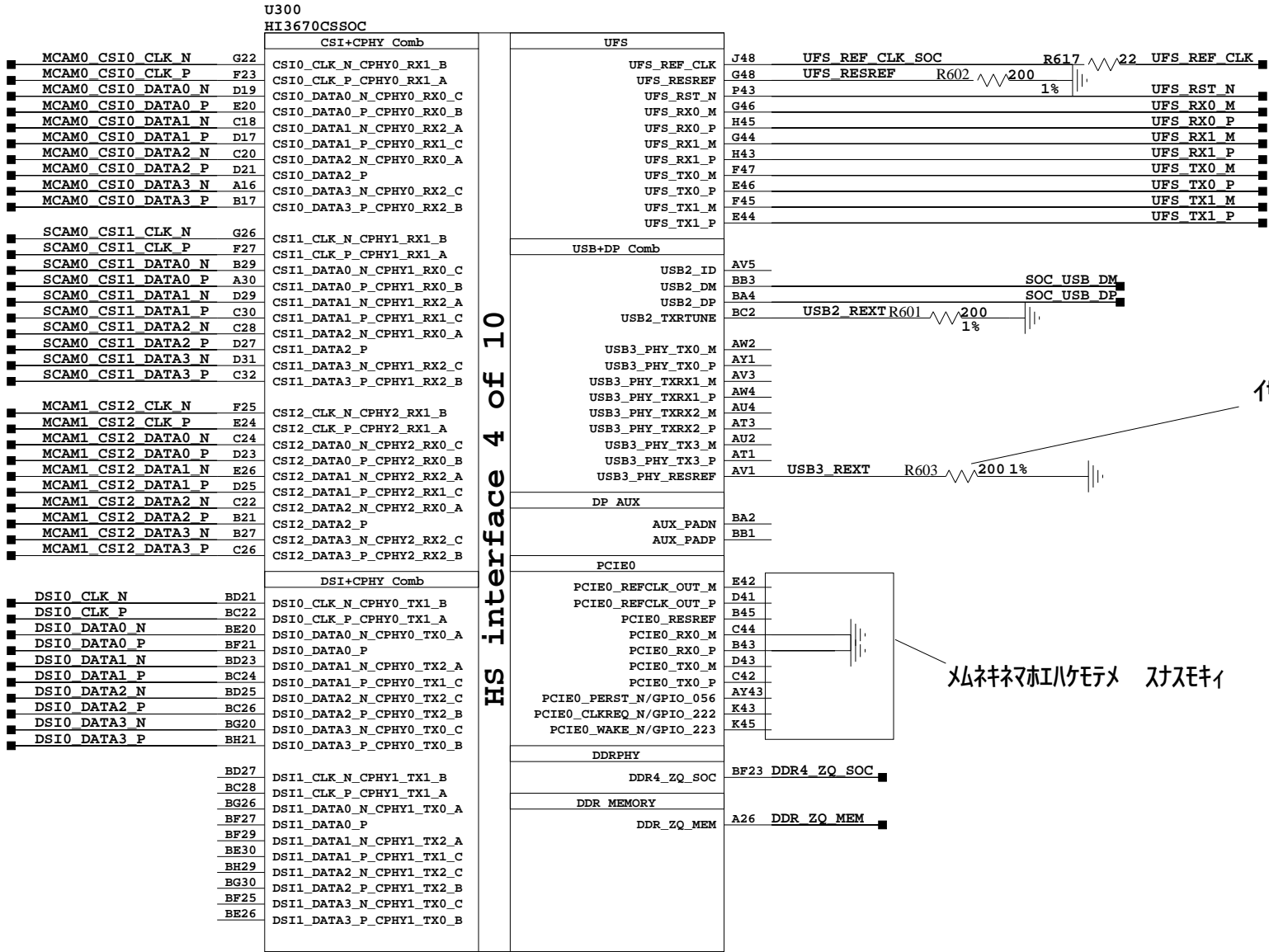
イヨアウヨPCIE「ヤコ」ヒシxuxiaoweiネネマエヒネ ツオ醸エハ菠 モリ

5.SOC PWR3



イサヨウヨPCIE「ヤコ」ヒシxuxiaoweiネキネミエヒネ ツキオ釀エハ菠 モオリ

6.SOC HS INTERFACE



7.SOC GPIO INTERFACE1

U300
HI3670CSSOC

ISP AO

GPIO_202/ISP_GPIO03_PRTB V45
CODEC_SSI_DATA U46
GPIO_201/PMU1_IRQ_N AF47
GPIO_208/MIC_GND_EN AN46
GPIO_207/AG_INT AL48
GPIO_218/BFGN_WAKEUP_AP N46
GPIO_219/DIR_CHG_INT AU44

IO AO

GPIO_177/WL_WAKEUP_AP U44
GPIO_182/ANT_DET0 AK43
GPIO_183/HIFI_PDN AB47
CD_CABC_PWM AN44
LCD_BL_PWM AR46
SLIMBUS_CLK W48
SLIMBUS_DATA V47
GPIO_194/DIR_CHG_EN AC44
GPIO_195/I2S_DO AG46
GPIO_196/I2S_CLK AT43
GPIO_197/I2S_FS AH47
GPIO_201/NFC_INT L46
GPIO_200/SPK_SMARTPA_INT J46
GPIO_205/SIM0_DET M45
GPIO_210/FP_INT AG48
GPIO_211/FP_WUHB_INT AP47
GPIO_212/TP_INT_N AP45
GPIO_220/CHG_VBST_CTRL V43
GPIO_221/CC_INT W44
PMU0_INT R44

SPI AO

SPI0_CLK/GPIO_227/I3C_SCL Y45
SPI0_DI/GPIO_228/I3C_SDA W46
SPI0_DO/GPIO_229 Y43
SPI0_CS0_N/GPIO_230 T45
SPI0_CS1_N/GPIO_231 AA44
SPI2_CLK/GPIO_213 SPI4_CLK
SPI2_DI/GPIO_214 SPI4_DI
SPI2_DO/GPIO_215 SPI4_DO
SPI2_CS0_N/GPIO_216 SPI4_CS0_N
SPI2_CS1_N/GPIO_217 SPI4_CS1_N
SPI3_CLK/GPIO_144 AE42
SPI3_DI/GPIO_145 AD43
SPI3_DO/GPIO_146 AC46
SPI3_CS0_N/GPIO_147 AD45
I2C7_SCL L44
SPI3_CS1_N/GPIO_148 I2C7_SDA
SPI3_CS2_N/GPIO_149 T43
CDMA_GPS_SYNC T43
CDMA_GPS_SYNC/LTE_GPS_SYNC

ISP PERI

ISP_GPIO00_FTRSTN BG40
GPIO_012 F5
ISP_GPIO01_BKRSTN E4
GPIO_013
ISP_GPIO02_MNTRB BF41
GPIO_014
ISP_GPIO06_FSYNC GPIO_015
ISP_GPIO10_SBPWM BB45
GPIO_016/LCD_TE0
ISP_CLK0/GPIO_017 B5
ISP_CLK1/GPIO_018 C6
ISP_CLK2/GPIO_019 D5
ISP_SCL0/GPIO_020 G4
ISP_SDA0/GPIO_021 G6
ISP_SCL1/GPIO_022 C34
ISP_SDA1/GPIO_023 A34
ISP_SCL2/GPIO_024 H5
ISP_SDA2/GPIO_025 H3

SYS SIGNAL

CLK_SLEEP N44
CLK_SYSTEM M43
SYSCLK_EN N48

PMU IF

SPMI_CLK/GPIO_226 T47
SPMI_DATA/GPIO_225 R46
PMU0_SSI B35
PMU_AUXDAC0_SSI/GPIO_019 D35
PMU_PERI_EN L48
PMU_RST_OUT_N K47
SOC_RST_PMU_N M47
PMU_PWR_HOLD P45

BOOT CTRL

BOOT_MODE AU48
DFT_EN AC48
TEST_MODE/GPIO_001 AU42

LTE CTRL

LTE_INACTIVE/GPIO_051 AY47
FRAME_SYNC
UART_RXD_BBP/UART_CTS_N_MHS
LTE_RX_ACTIVE/GPIO_052 AW46
UART_RXD_BBP/UART_RXD_MHS
LTE_TX_ACTIVE/GPIO_053 AW46
UART_TXD_BBP/UART_TXD_MHS
ISM_PRIORITY/GPIO_054 AV47
UART_RTS_N_MHS

PERI IO

GPS_REF/GPIO_005 BC42
PWM_OUT1/GPIO_017 BH41
GPIO_000_SWP BG38
GPIO_055_ONEWIRE BF35

GPIO_012 LCM_ENP BG40
GPIO_013 MCAM0_RST F5
GPIO_015 TP_RST_N BF41
LCD_TE0 BB45
ISP_CLK0 MCAM0 R722
ISP_CLK1 SCAM R723
ISP_CLK2 MCAM1 R724
ISP_SCL0 MCAM0 G4
ISP_SDA0 MCAM0 G6
ISP_SCL1 SCAM C34
ISP_SDA1 SCAM A34
ISP_SCL2 MCAM1 H5
ISP_SDA2 MCAM1 H3

PMU0_CLK32_SYS N44
SYS_CLK M43
SYS_CLK_EN N48

R743 22SPMI_CLK
SPMI_DATA
PMU_AUXDAC0_SSI
PMU_PERI_EN
PMU_RST_OUT_N
SOC_RST_PMU_N
PMU_PWR_HOLD

BOOT_MODE
DFT_EN
TP712

GPIO_051 LTE_INACTIVE AY47
GPIO_052 LTE_RX_ACTIVE AW46
GPIO_053 LTE_TX_ACTIVE AW46
GPIO_054 ISM_PRIORITY AV47

AP_GPS_REF_CLK BC42
GPIO_017_OVP_CTRL BH41
GPIO_055_SEC BF35

GPIO_031_CODEC_RST_N BC44
GPIO_032_SCAM_RST_N BD45
GPIO_033_PMU1_EN BD43
GPIO_034_LCM_ENN BC46
GPIO_039_Hi1102_PWRON AV45
GPIO_010_NFC_EN AU44
GPIO_029_LCD0_RST_N BE42
GPIO_030_NFC_DWL_REQ BE44

GPIO_012 LCM_ENP BG40
GPIO_013 MCAM0_RST F5
GPIO_015 TP_RST_N BF41
LCD_TE0 BB45
ISP_CLK0 MCAM0 R722
ISP_CLK1 SCAM R723
ISP_CLK2 MCAM1 R724
ISP_SCL0 MCAM0 G4
ISP_SDA0 MCAM0 G6
ISP_SCL1 SCAM C34
ISP_SDA1 SCAM A34
ISP_SCL2 MCAM1 H5
ISP_SDA2 MCAM1 H3

PMU0_CLK32_SYS N44
SYS_CLK M43
SYS_CLK_EN N48

R743 22SPMI_CLK
SPMI_DATA
PMU_AUXDAC0_SSI
PMU_PERI_EN
PMU_RST_OUT_N
SOC_RST_PMU_N
PMU_PWR_HOLD

BOOT_MODE
DFT_EN
TP712

GPIO_051 LTE_INACTIVE AY47
GPIO_052 LTE_RX_ACTIVE AW46
GPIO_053 LTE_TX_ACTIVE AW46
GPIO_054 ISM_PRIORITY AV47

AP_GPS_REF_CLK BC42
GPIO_017_OVP_CTRL BH41
GPIO_055_SEC BF35

GPIO_031_CODEC_RST_N BC44
GPIO_032_SCAM_RST_N BD45
GPIO_033_PMU1_EN BD43
GPIO_034_LCM_ENN BC46
GPIO_039_Hi1102_PWRON AV45
GPIO_010_NFC_EN AU44
GPIO_029_LCD0_RST_N BE42
GPIO_030_NFC_DWL_REQ BE44

I2C AO

I2C0_SCL/GPIO_186/I3C_SCL AM47
I2C0_SDA/GPIO_187/I3C_SDA AN48
I2C1_SCL/GPIO_188 AK45
I2C1_SDA/GPIO_189 AK47
I2C7_SCL AT47
I2C7_SDA AR44
GPIO_150_CHG_INT AG44
GPIO_151_LCD0_ID1 AH45
I2C6_SCL AL44
I2C6_SDA AM43

I2C PERI

I2C3_SCL/GPIO_006 BG42
I2C3_SDA/GPIO_007 BF43
I2C4_SCL/GPIO_026 BG44
I2C4_SDA/GPIO_027 BF45
PMUI2C0_SCL/GPIO_003 AR42
PMUI2C0_SDA/GPIO_001 AP43

UART

UART2_CTS_N/GPIO_031 BC44
UART0_RXD BD45
UART2_RTS_N/GPIO_032/LCD_TE0 BD45
UART0_TXD BD43
UART2_TXD/GPIO_033 BD43
UART0_RTS_N BD43
UART2_RXD/GPIO_034 BC46
UART0_CTS_N BC46
UART3_CTS_N/GPIO_039 AV45
UART5_CTS_N AU44
UART3_RTS_N/GPIO_010 AU44
UART5_RTS_N AW44
UART3_RXD/GPIO_011 AV43
UART5_RXD AV43
UART3_TXD/GPIO_012 AV43
UART5_TXD AV43
UART4_CTS_N/GPIO_013 BA44
UART4_RTS_N/GPIO_014 BA46
UART4_RXD/GPIO_015 BA48
UART4_TXD/GPIO_016 AY45
UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37

SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37

SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37

SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
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SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
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UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
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SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37

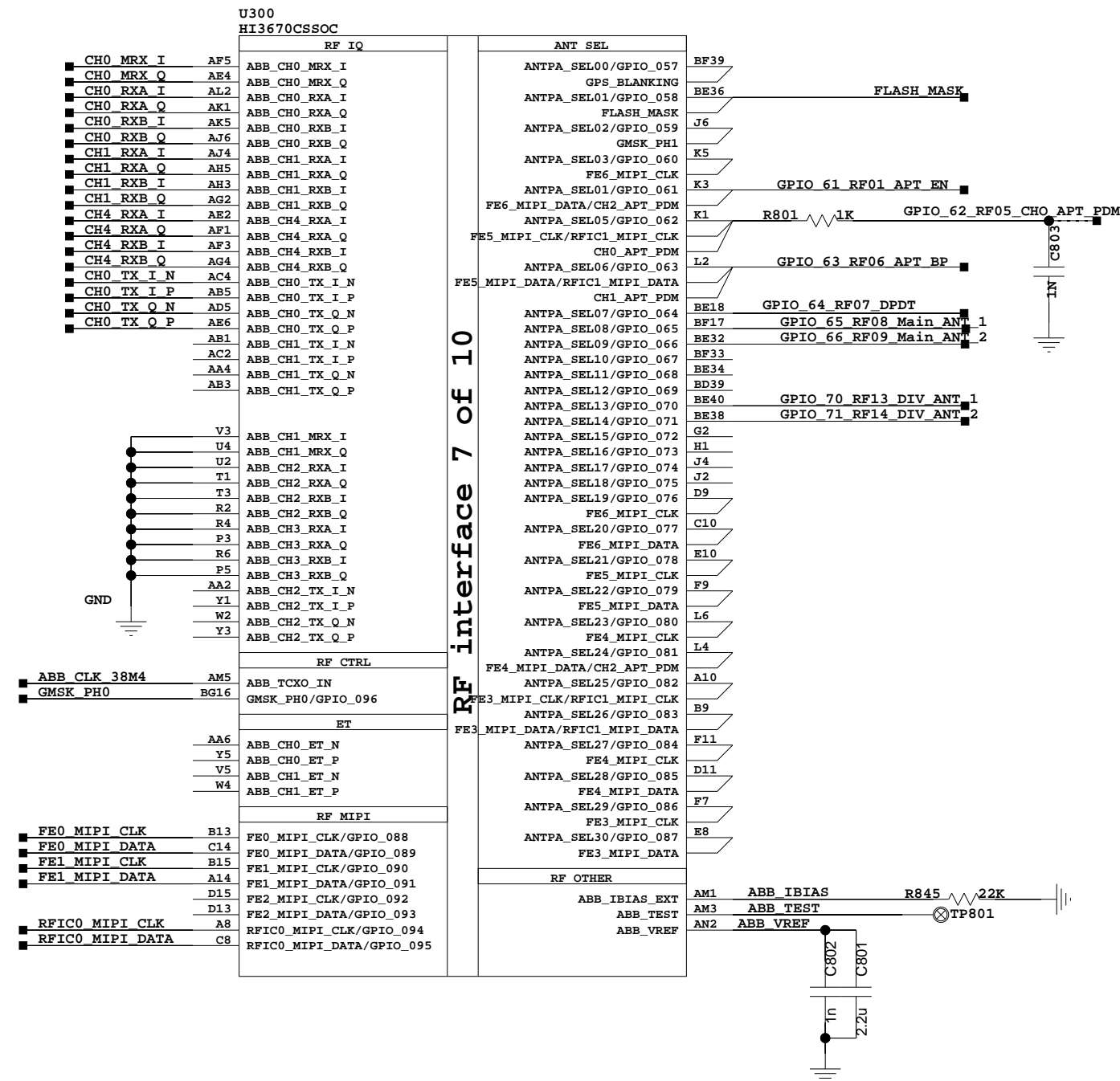
SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37

SOC_BFGN_UART4_CTS_N BA44
SOC_BFGN_UART4_RTS_N BA46
SOC_BFGN_UART4_RXD BA48
SOC_BFGN_UART4_TXD AY45

UART6_CTS_N/GPIO_035 AU46
UART0_RXD AT45
UART6_RTS_N/GPIO_036 AT45
UART0_TXD BG36
UART6_RXD/GPIO_037 BG36
UART0_RXD_M/UART_RXD_MHS BF37
UART6_TXD/GPIO_038 BF37
UART0_TXD_M/UART_TXD_MHS BF37</

8.SOC RF INTERFACE



RF interface 7 of 10

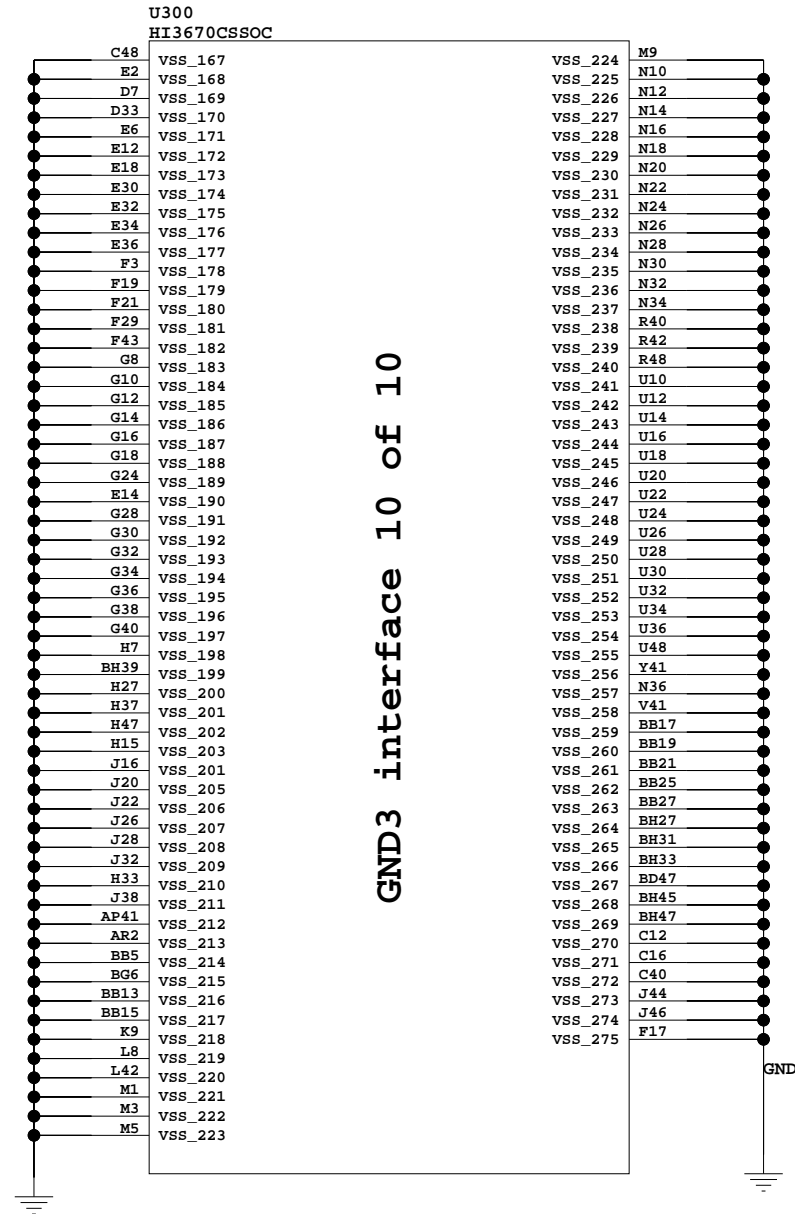
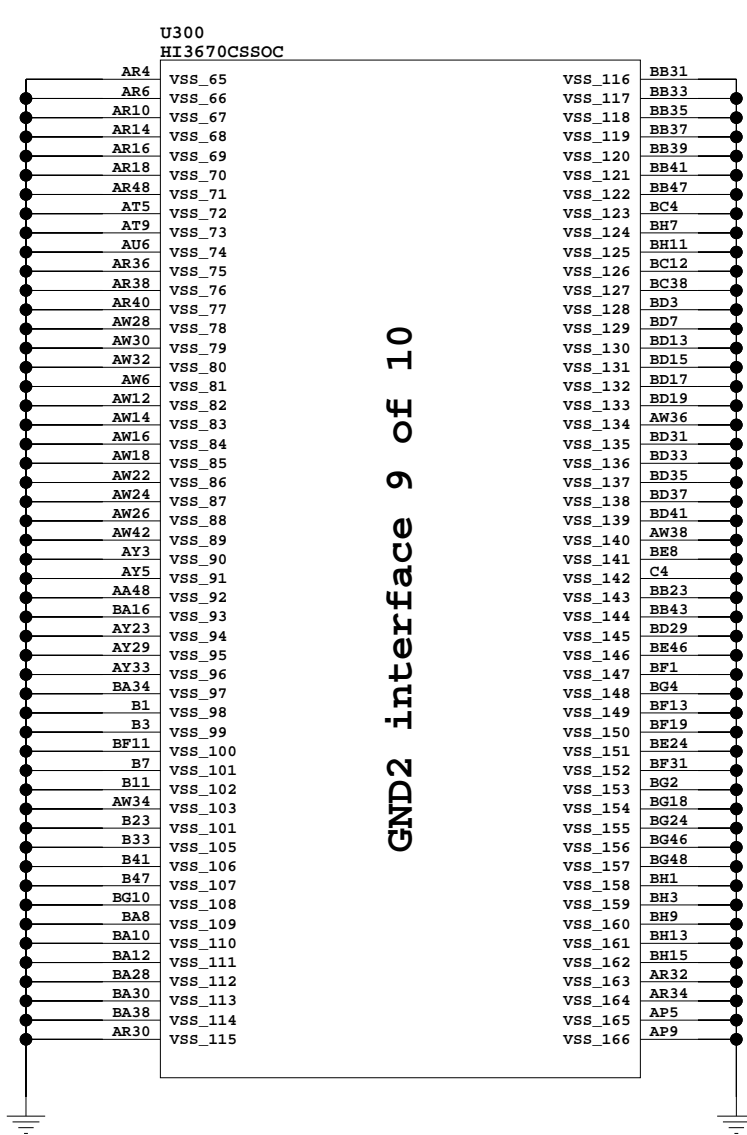
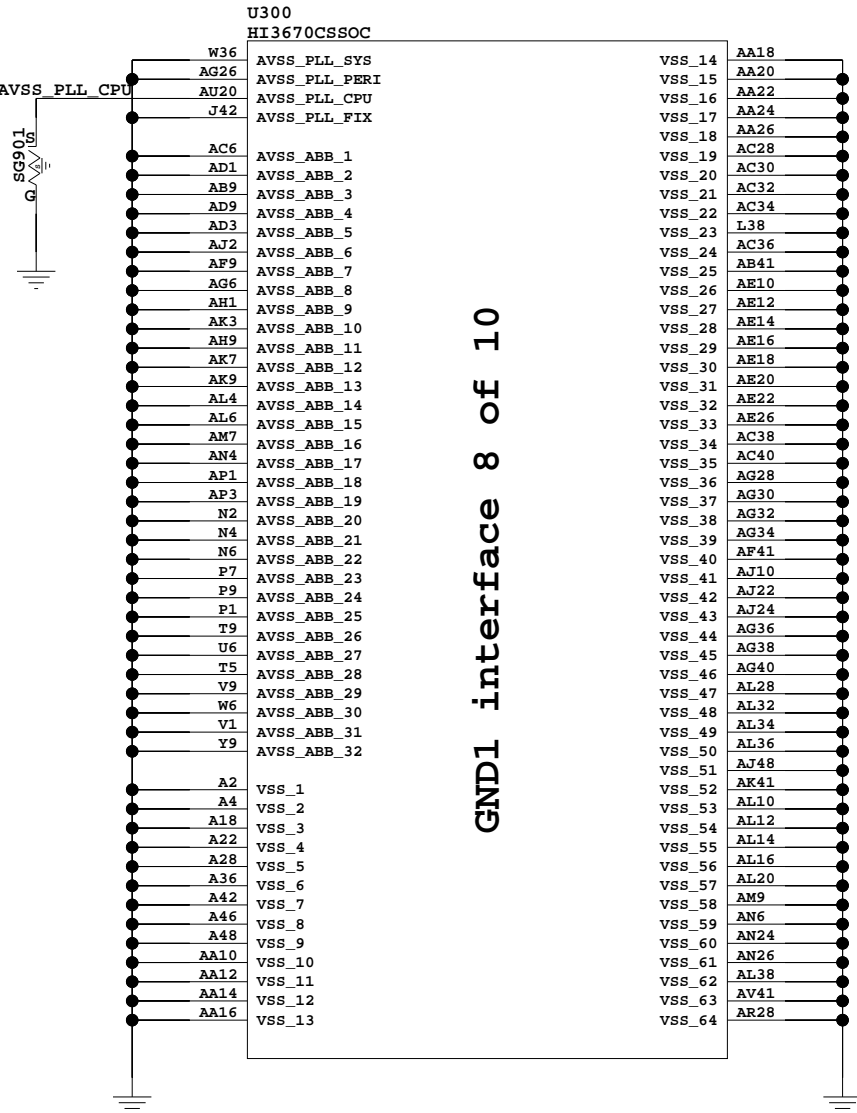
A

B

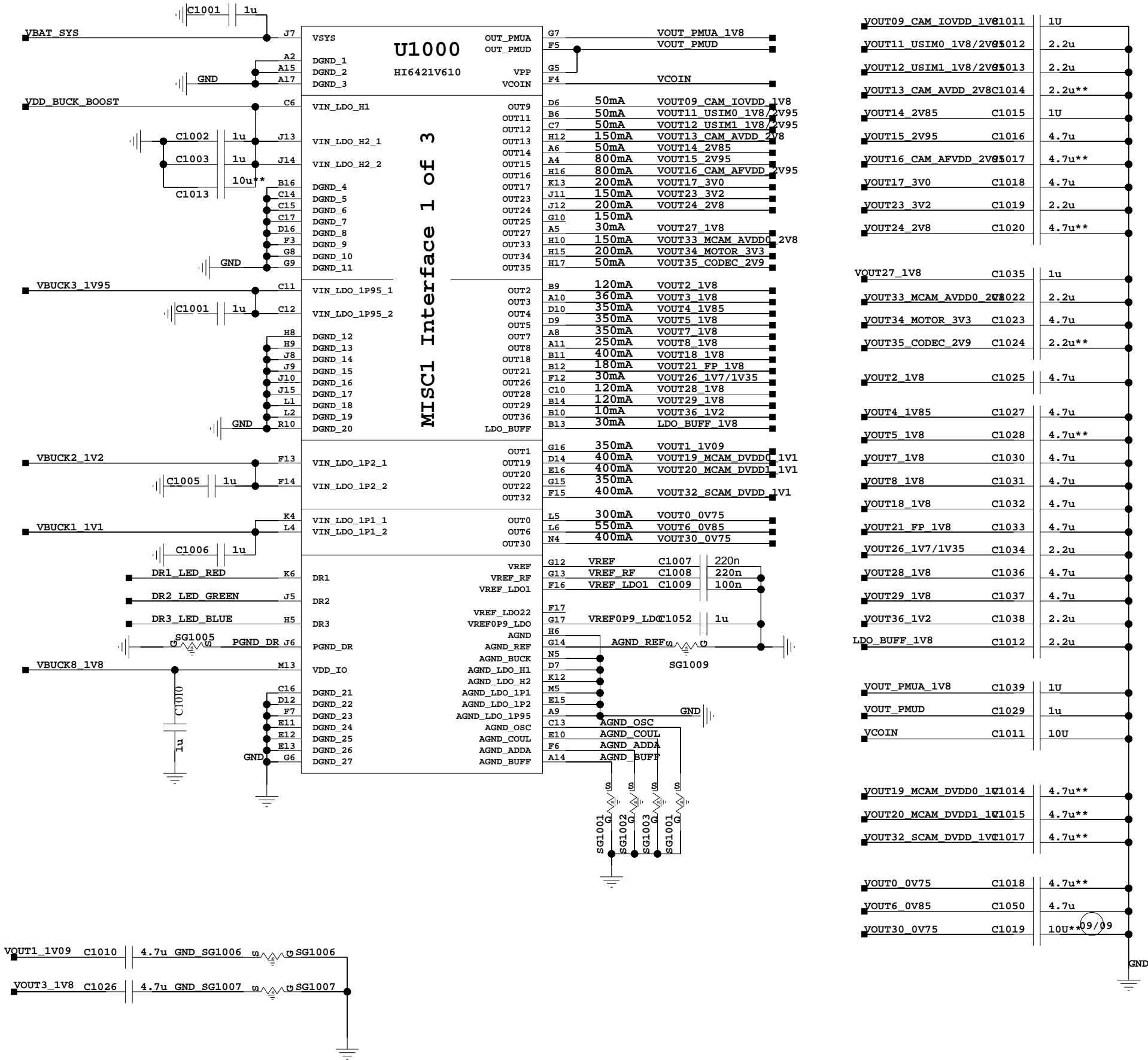
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D

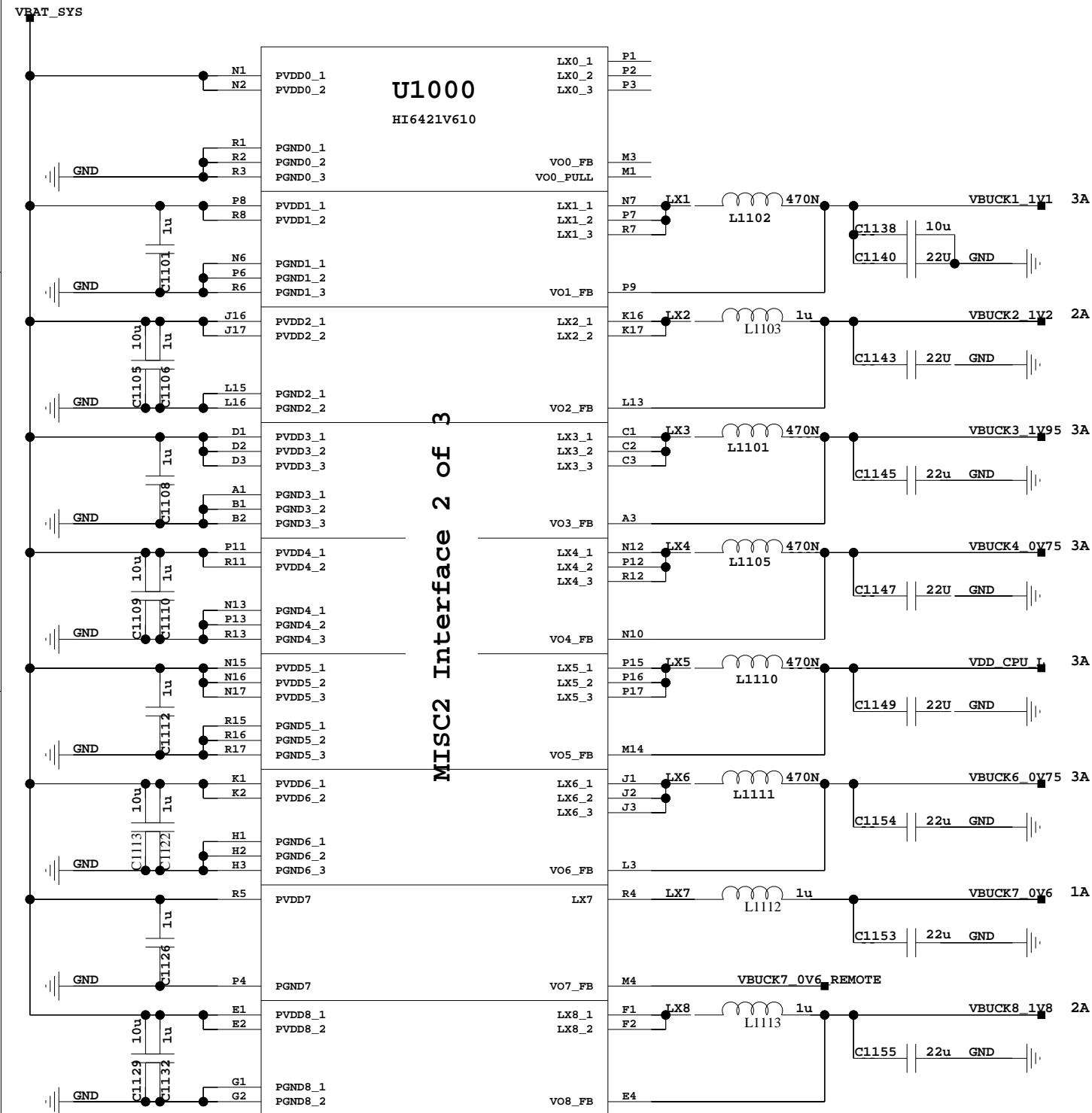
9.SOC GND



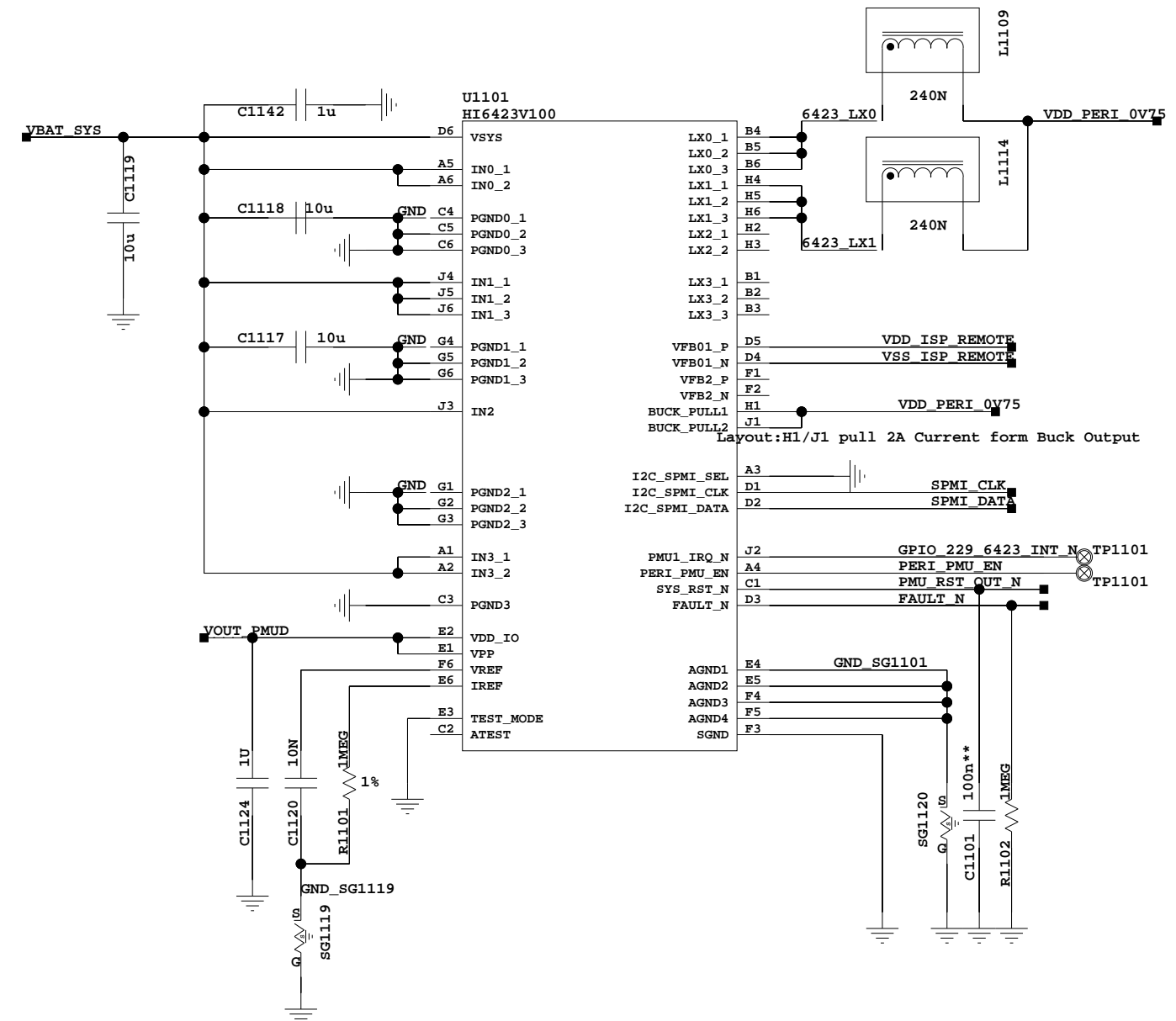
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	10. Hi6421 LDO					
	NUM	Vol	Current	Function		
A	LDO0	0.75	300	SOC:UFS,SYS,PLL_SYS		
	LDO1	1.09	350	RFIC0 AVDD10		
	LDO2	1.8	120	SOC_EFUSE&HISEE		
	LDO3	1.8	360	RFIC AVDD18,LNA_VDD		
	LDO4	1.85	350	LCD&TP 1.8V IO		
B	LDO5	1.8	350	SOC 1.8V AVDD		
	LDO6	0.85	550	SOC:AVDD085_ABB		
	LDO7	1.8	350	SOC:AVDD18_ABB		
	LDO8	1.8	250	IOVDD FOR Codec/RF		
	LDO9	1.8	50	SOC SDIO 1.8V		
C	LDO11	1.8/2.95	50	SIM0		
	LDO12	1.8/2.95	50	SIM1		
	LDO13	2.8	150	CAM_AVDD		
	LDO14	2.85	50	RF switch VDD		
	LDO15	2.95	800	UFS_VCC		
D	LDO16	2.95	800	SDCARD VDD		
	LDO17	3.3	200	MCAM_AFVDD1		
	LDO18	1.8	400	PERI IOVDD		
	LDO19	1.2	400	MCAM_DVDD0		
	LDO20	1.1	400	MCAM_DVDD1		
	LDO21	1.8	180	CAM IOVDD		
	LDO22	1.09	350	RESEVERD		
	LDO23	3.2	150	USB 2.0 PHY/PA		
	LDO24	2.8	200	Sensor VDD		
	LDO25	2.85	150	MCAM_AFVDD0		
	LDO26	1.7	30	38.4MHZ XO CORE		
	LDO27	1.8	30	HKADC,XOADC		
	LDO28	1.8	120	RF switch VIO		
	LDO29	1.8	120	VPH_UFS,PLL_SYS		
	LDO30	0.75	400	FIX 0.75 For SOC		
	LDO32	1.1	400	SlaveCAM_DVDD		
	LDO33	2.8	150	MCAM_AVDD0		
	LDO34	3.3	200	FP VDD		
	LDO35	2.9	50	CODEC_AVDD		
	LDO36	1.2	10	UFS refclk/rst IO		
	1	2	3	4	5	6



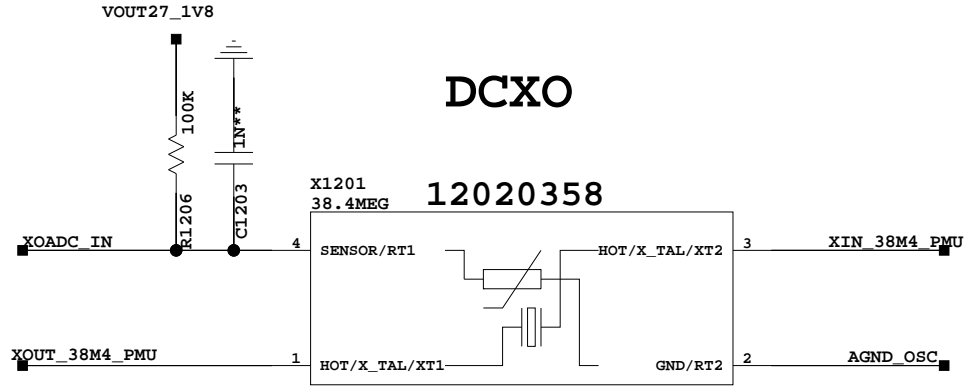
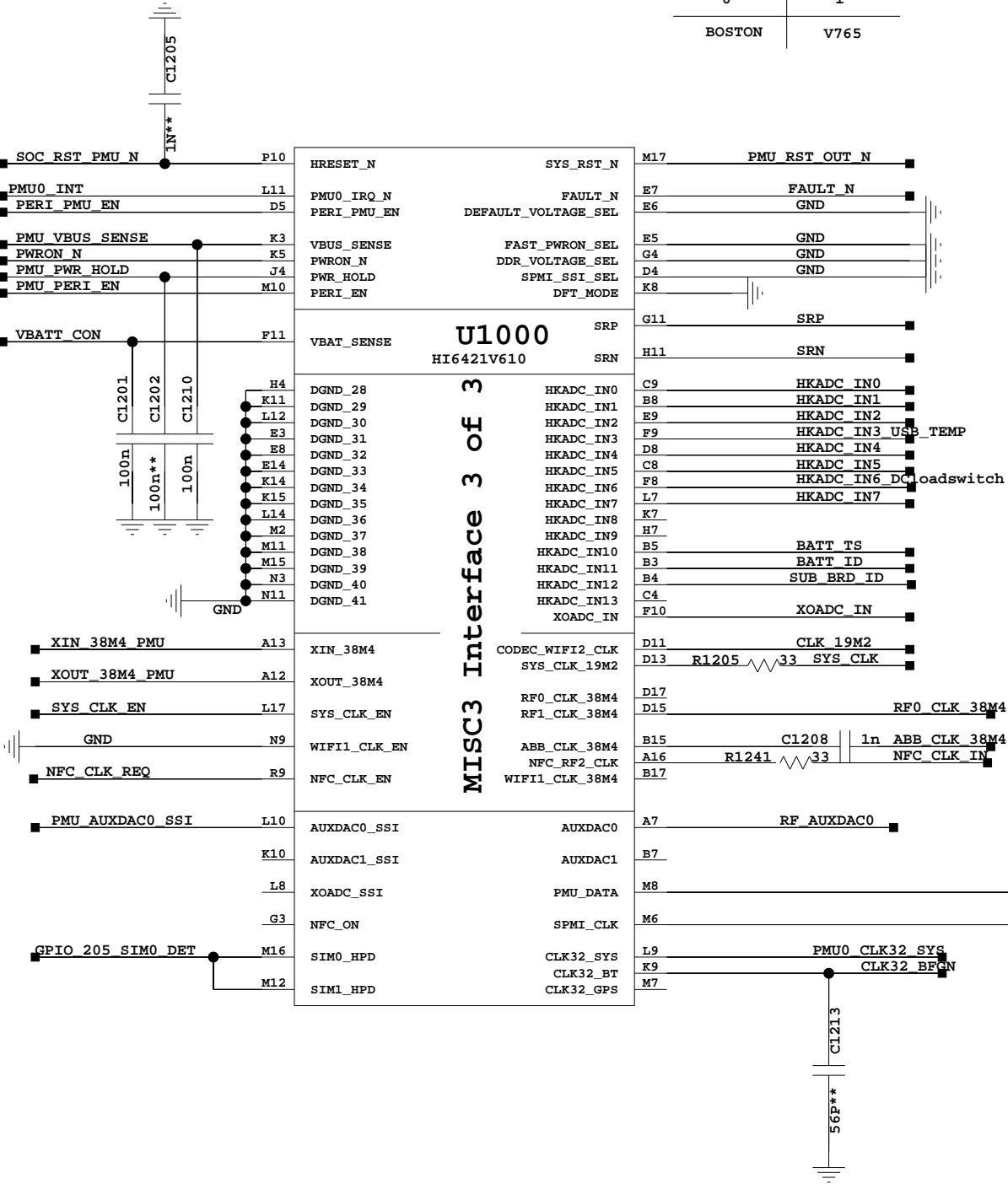
11.Hi6421 BUCK AND Hi6423



Hi 6423



12.Hi6421 DIGITAL INTERFACE



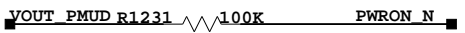
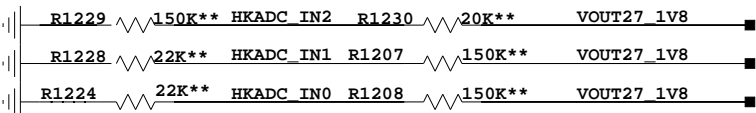
DEFAULT_VOLTAGE_SEL	
0	1
BOSTON	V765

FAST_PWRON_SEL	
0	1
NORMAL	FAST

DDR_VOLTAGE_SEL	
0	1
LPDDR4x	LPDDR3

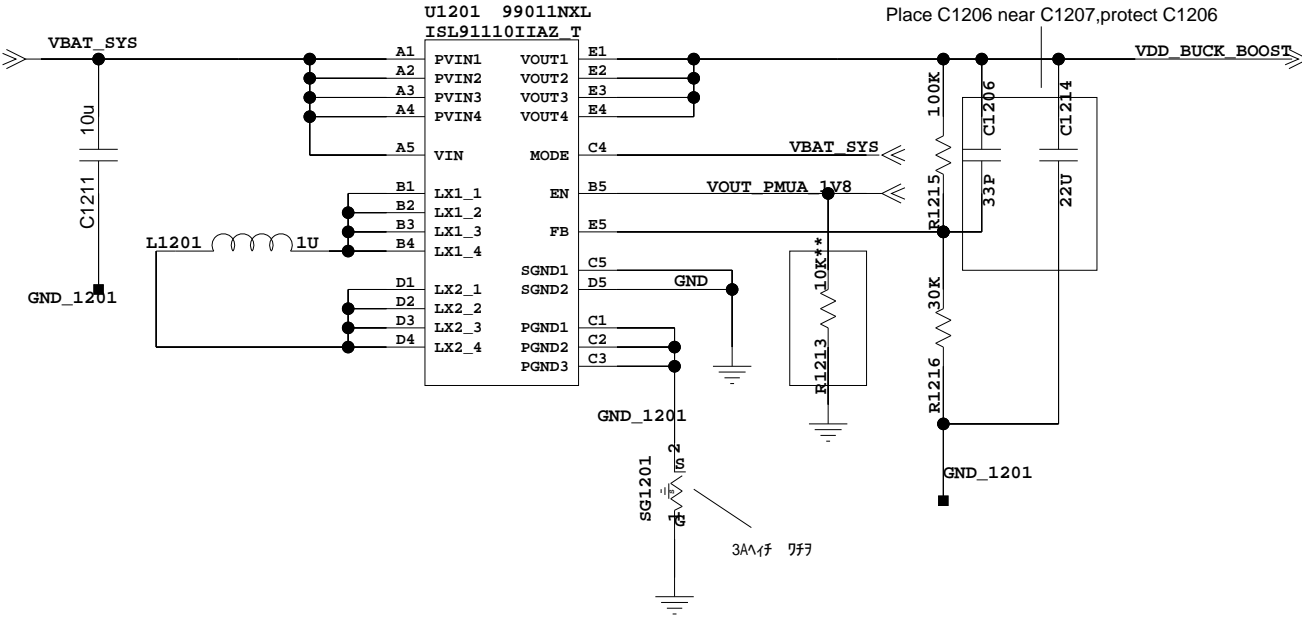
SPMI_SSI_SEL	
0	1
SPMI	SSI

HKADC_IN0	APU Board ID0
HKADC_IN1	APU Board ID1
HKADC_IN2	APU Board ID2
HKADC_IN3	TypeC TEMP
HKADC_IN4	RFIC0 TEMP
HKADC_IN5	Charge TEMP
HKADC_IN6	DCloadswitch TEMP
HKADC_IN7	AP TEMP
HKADC_IN8	RESERVED
HKADC_IN9	RFIC1 TEMP
HKADC_IN10	BATT TEMP
HKADC_IN11	BATT ID
HKADC_IN12	RESERVED
HKADC_IN13	RESERVED



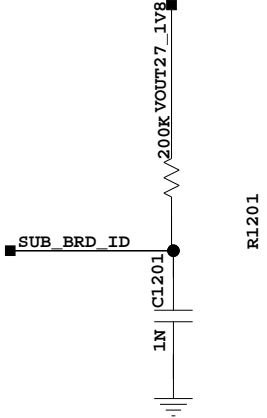
BUCK BOOST

$V_{out} = 0.8 * (1 + R1/R2) = 3.467V$

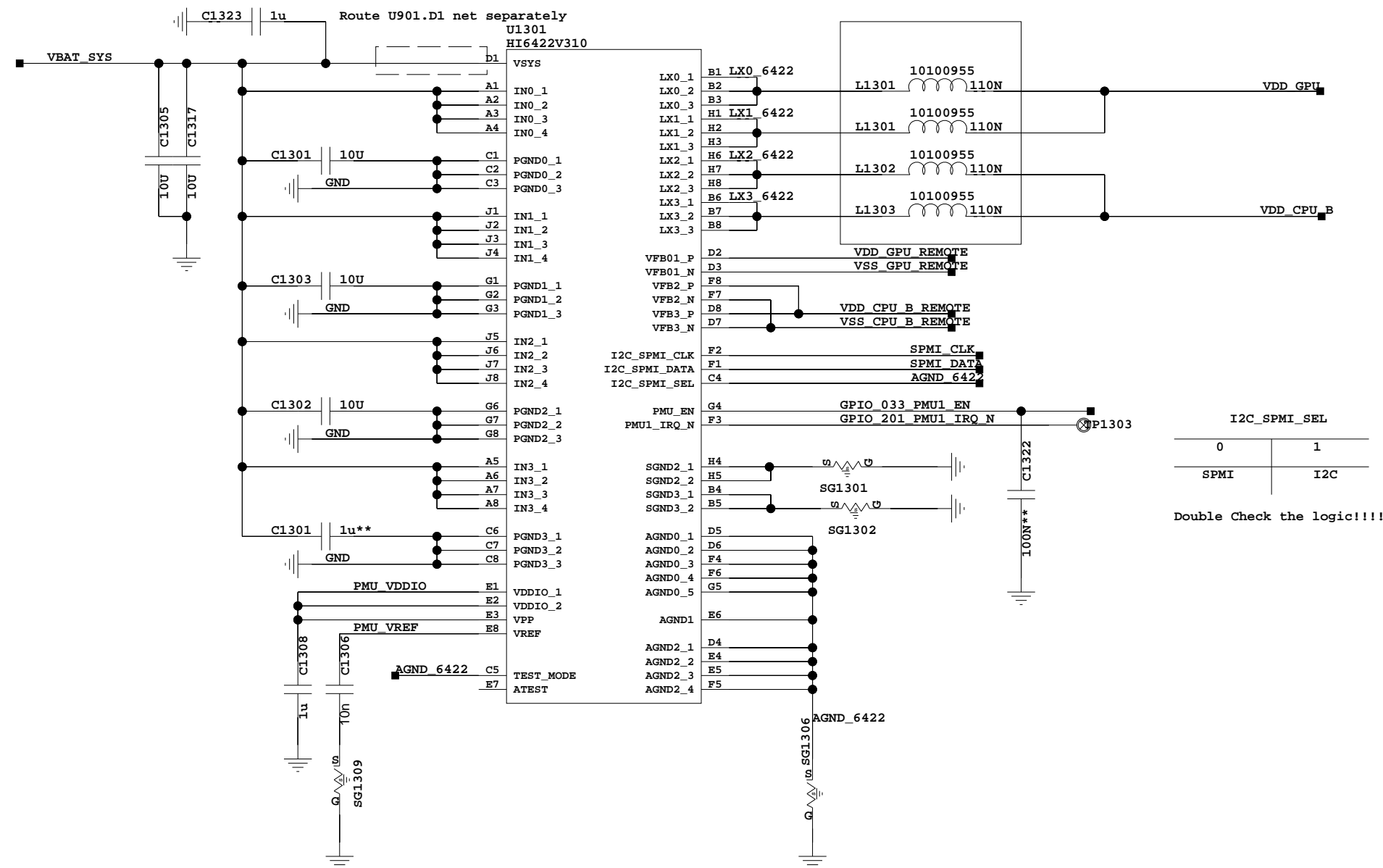


BOARD ID Table

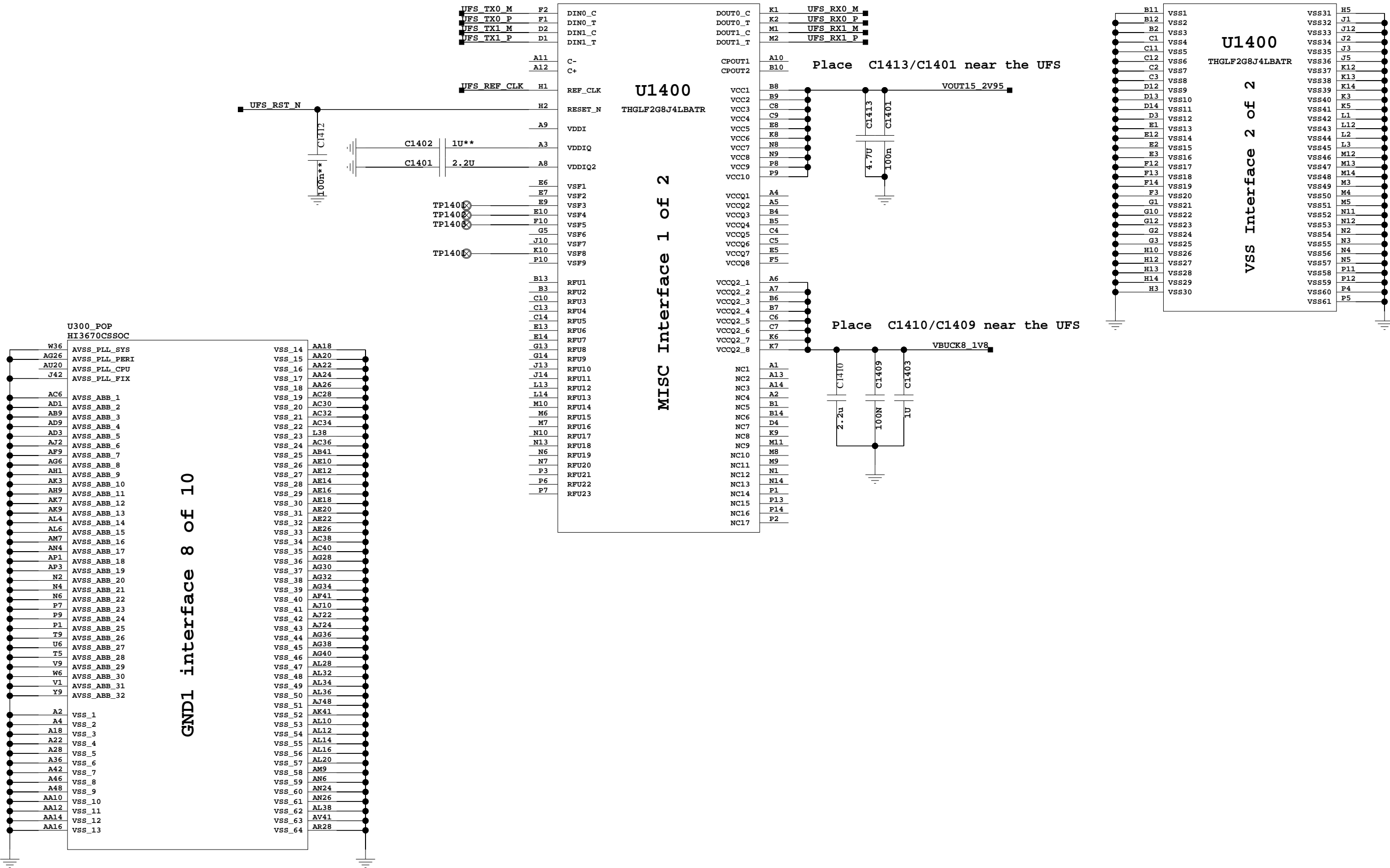
Board ID	HKADC2	HKADC1	HKADC0
V1			
V3			



13.Hi6422V300

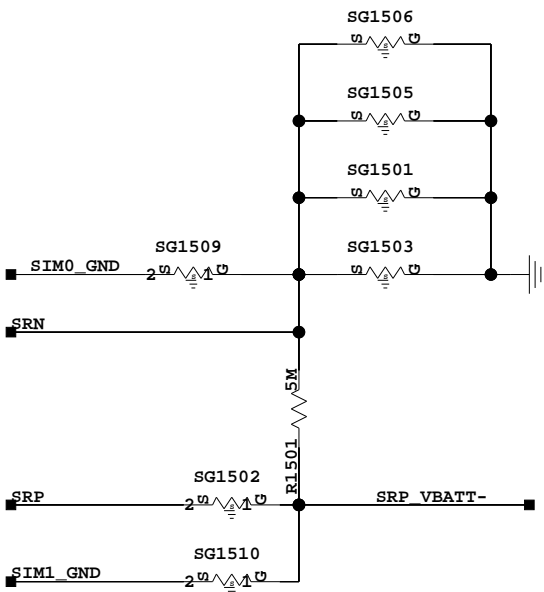
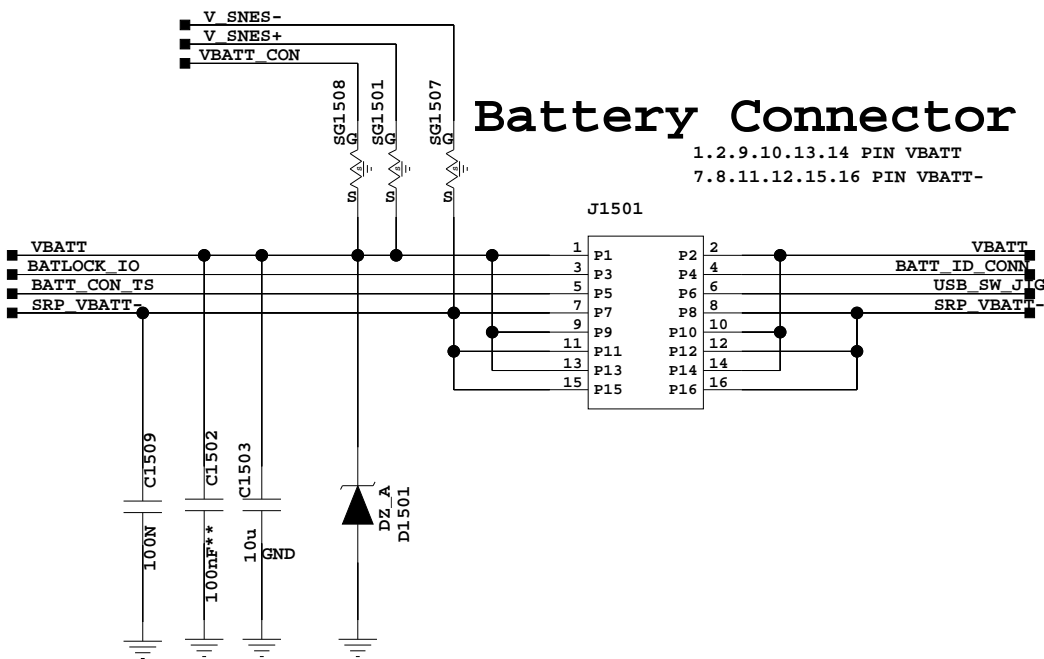


14.UFS and LPDDR4X

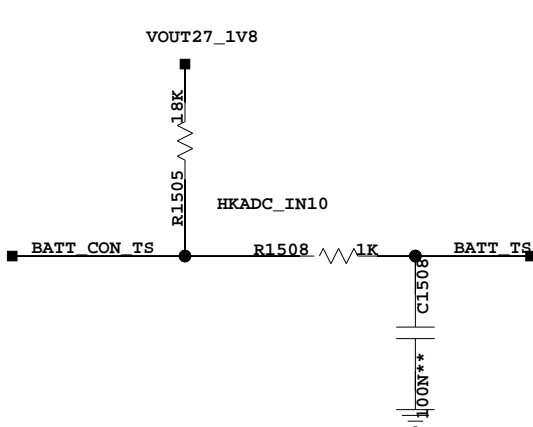


15. Battery & Fuel guage

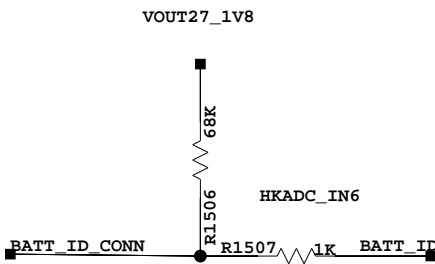
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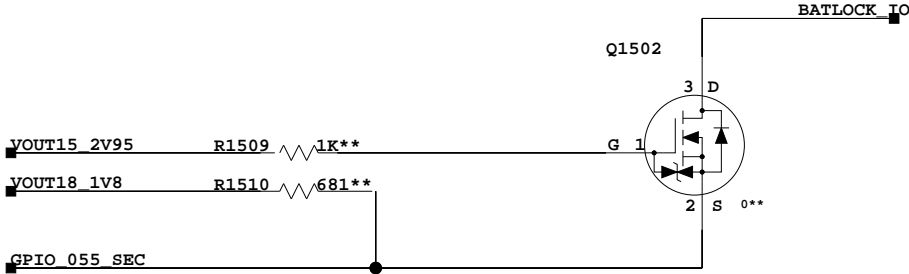
CAD note:
1.Trace for 9A
2.Differential trace



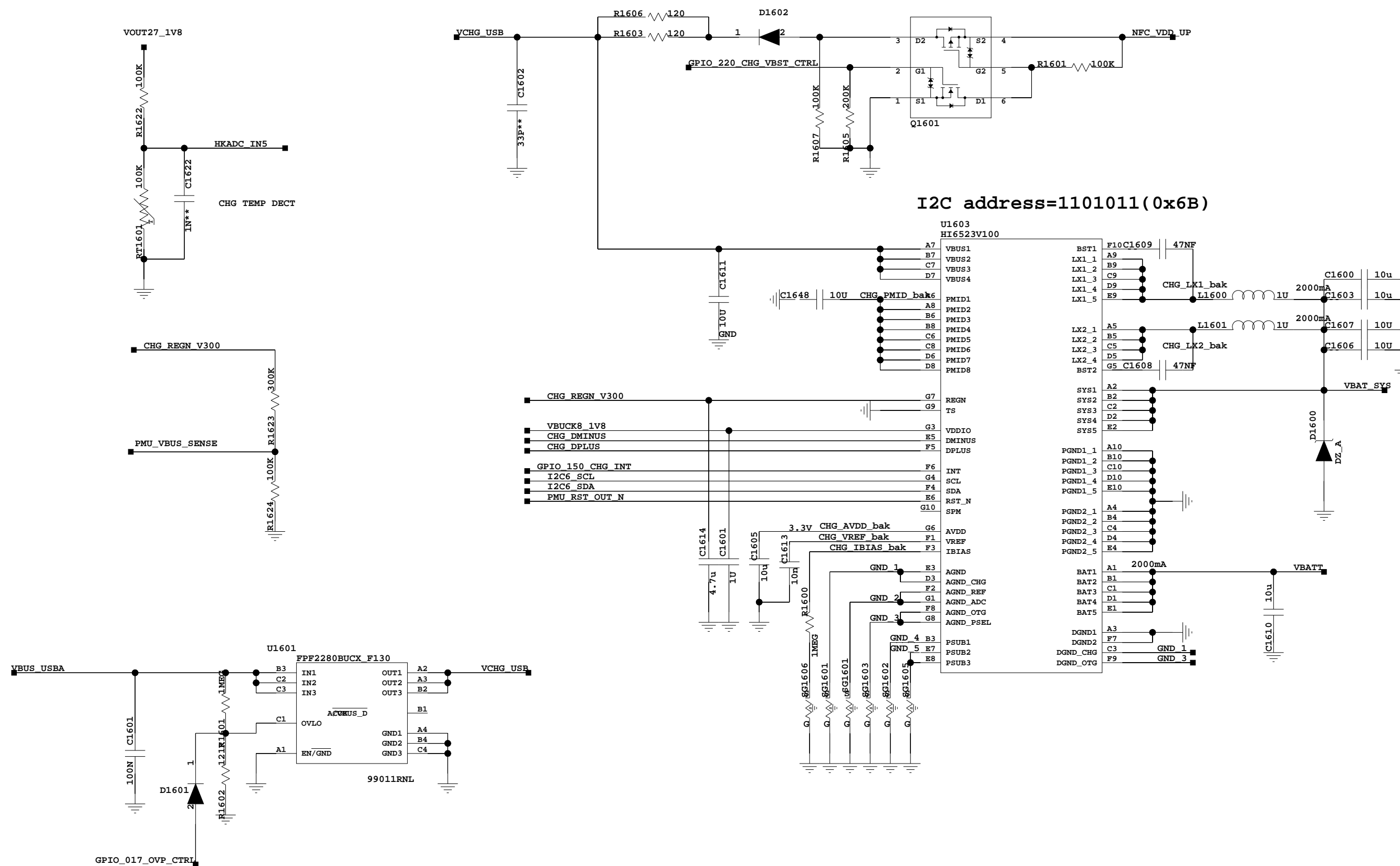
Battery Temperature



Battery ID



ヨウコ、ア

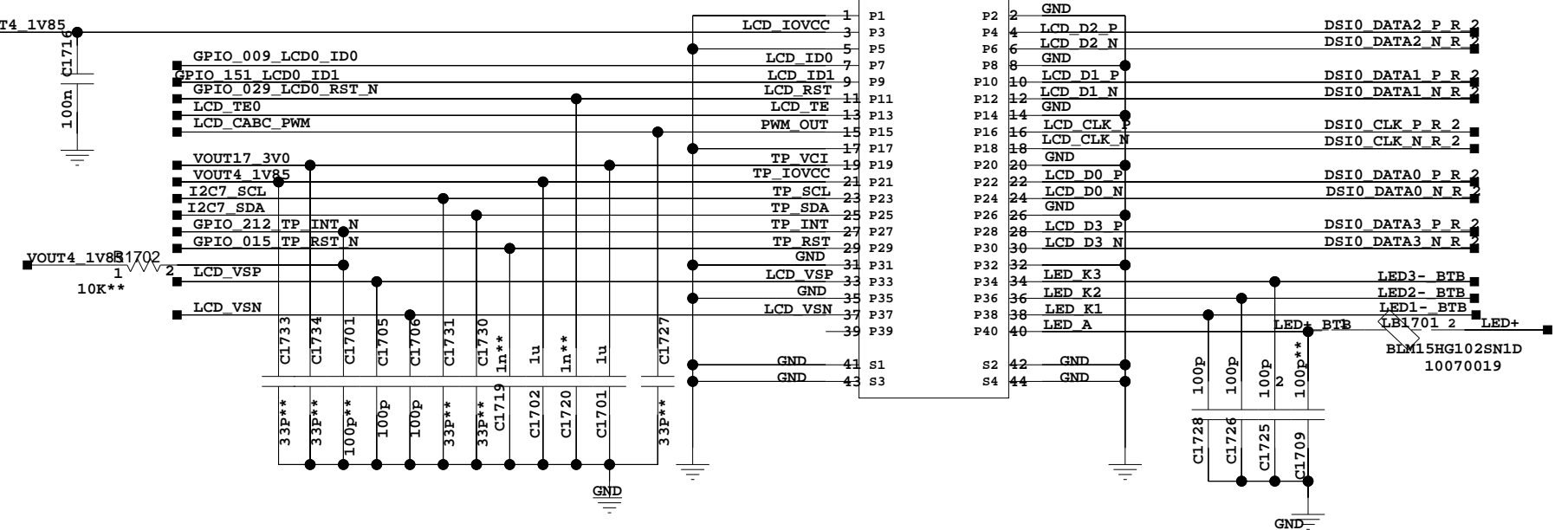
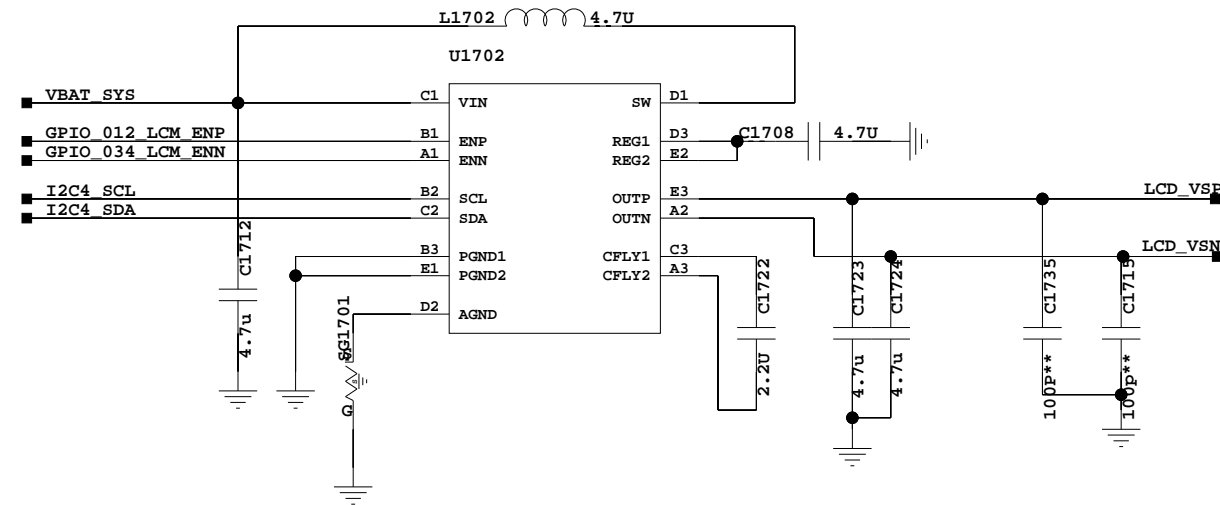


17. LCD Interface

LCD POS/NEG Voltage Driver

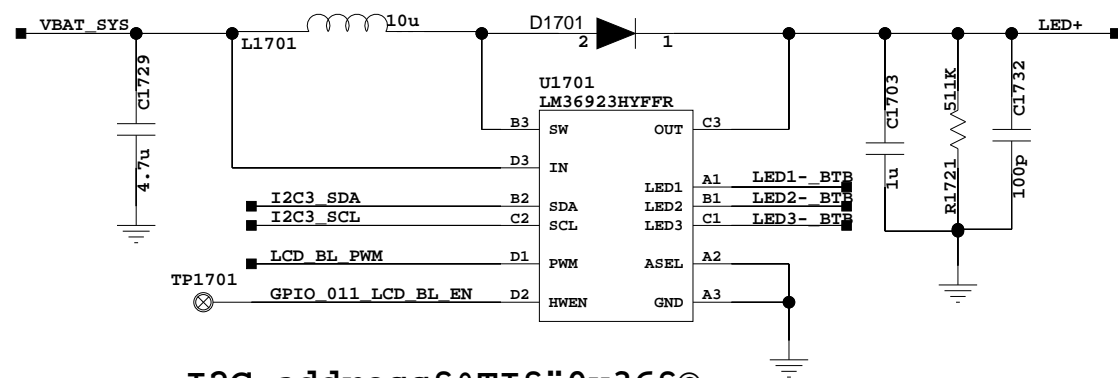
CAP tolerance above 50V!!!

I2C address 0x73 / 0x3E

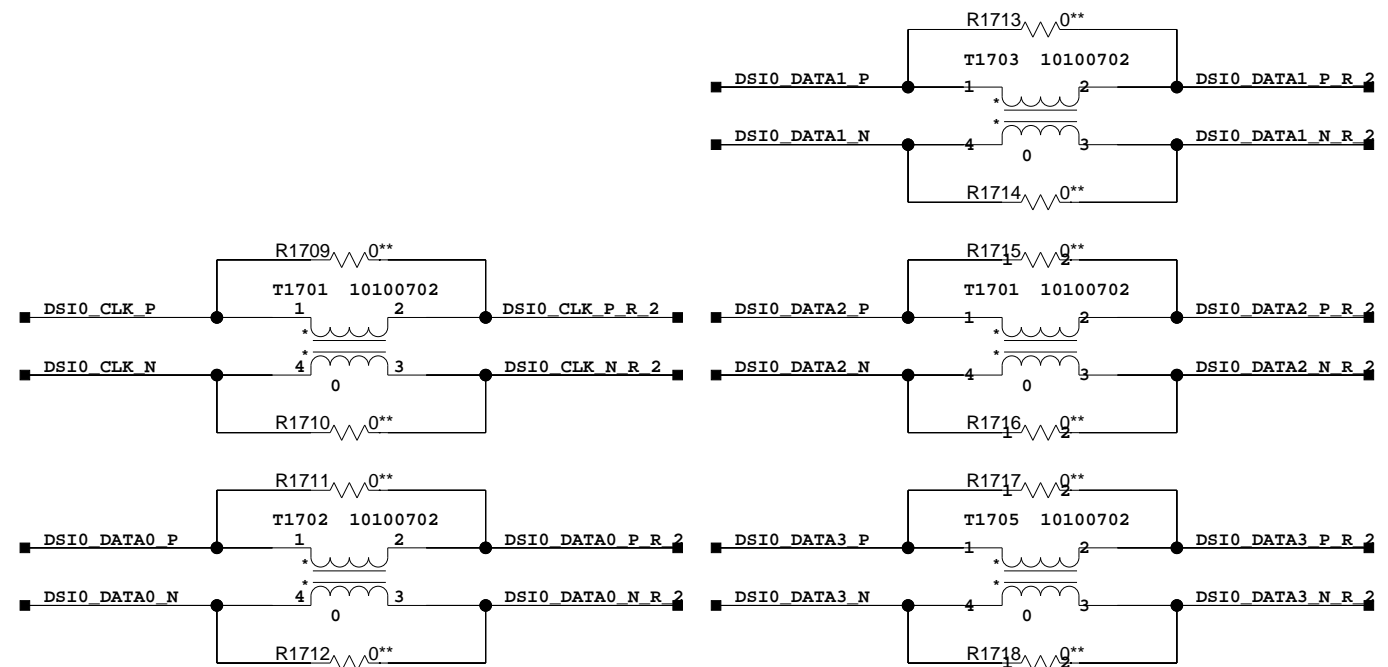


LCD BL DRIVER

CAP tolerance above 50V!!!



I2C address 0x36



ルモム

A



ハケ筆ニオツニヤ

B

C



D

D

19. M1/M2

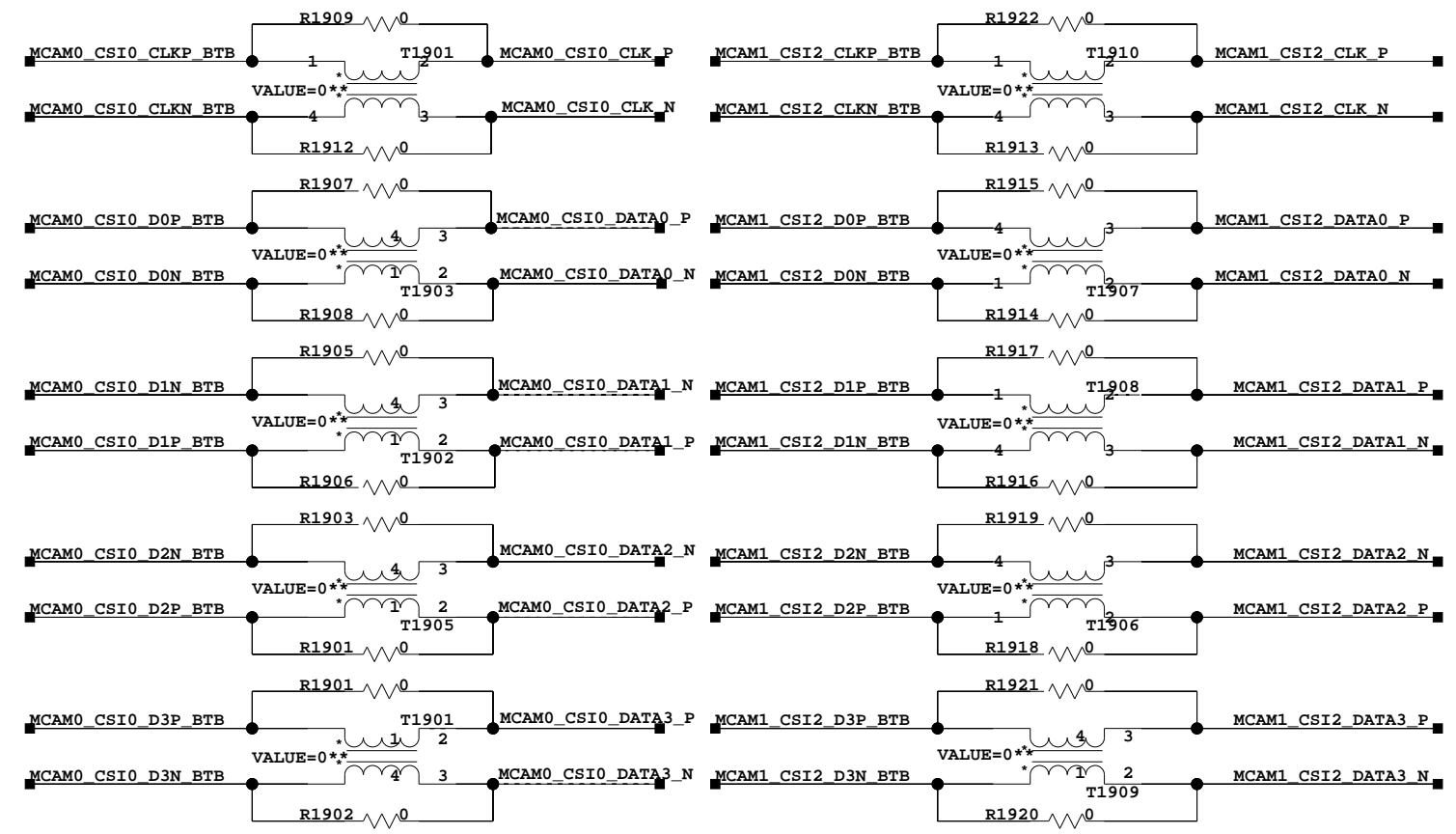
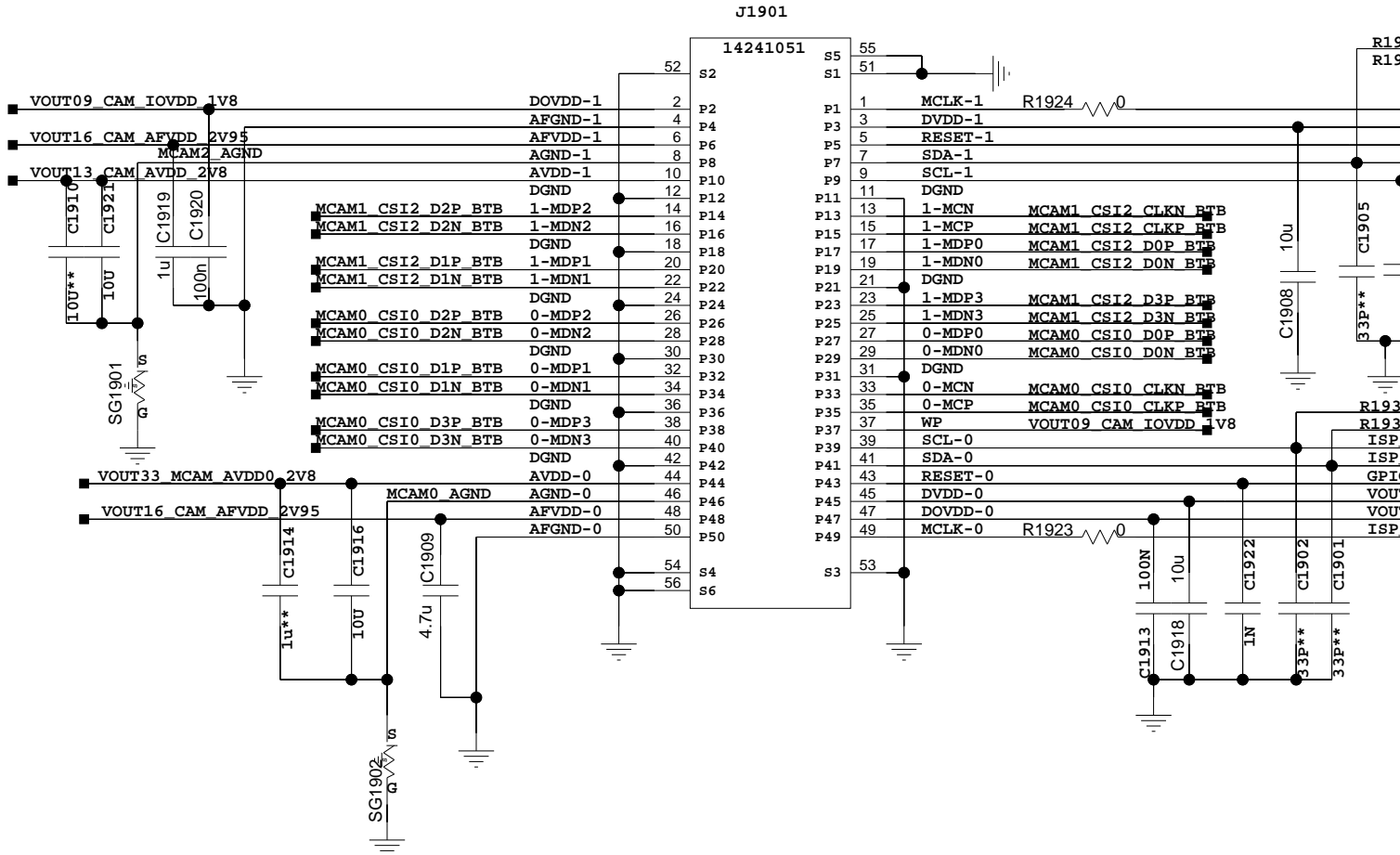
↑ ルーム

A

B

C

D

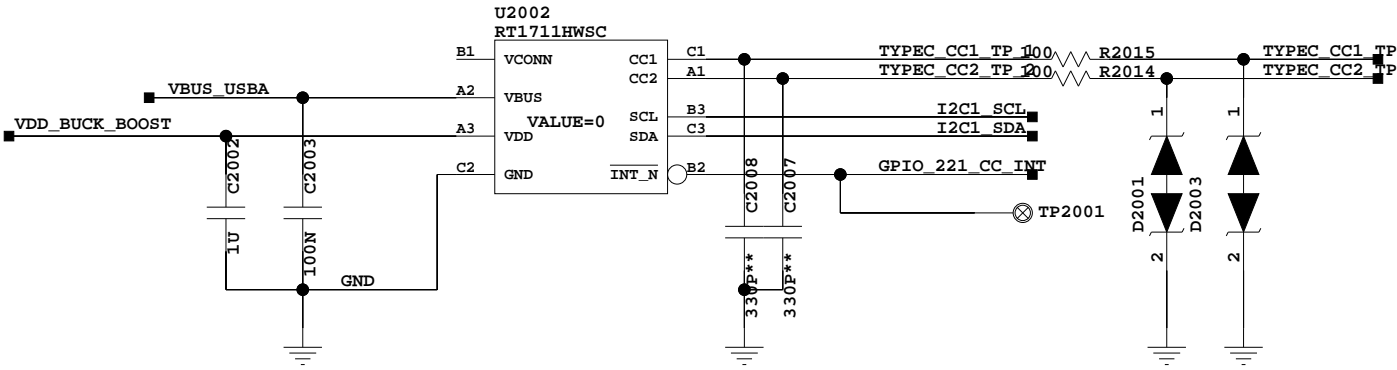
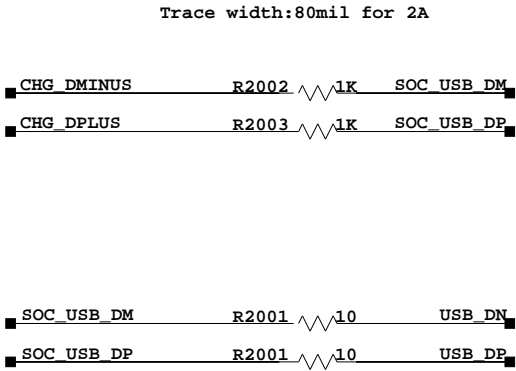


C19197 荳 旗-Jyサ-チホキ付材マツ 10/16

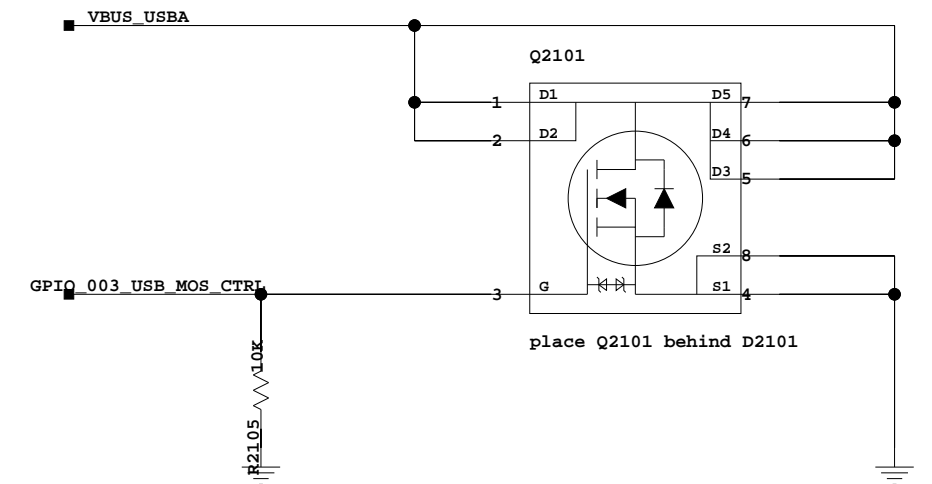
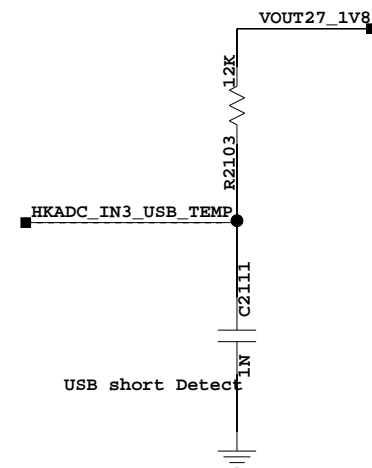
20. USB

אבכע

I2C Address:0X4E(Richtek)/0X20(TI)/0X22(FSC)



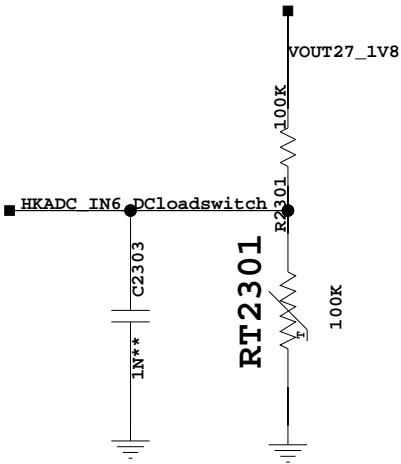
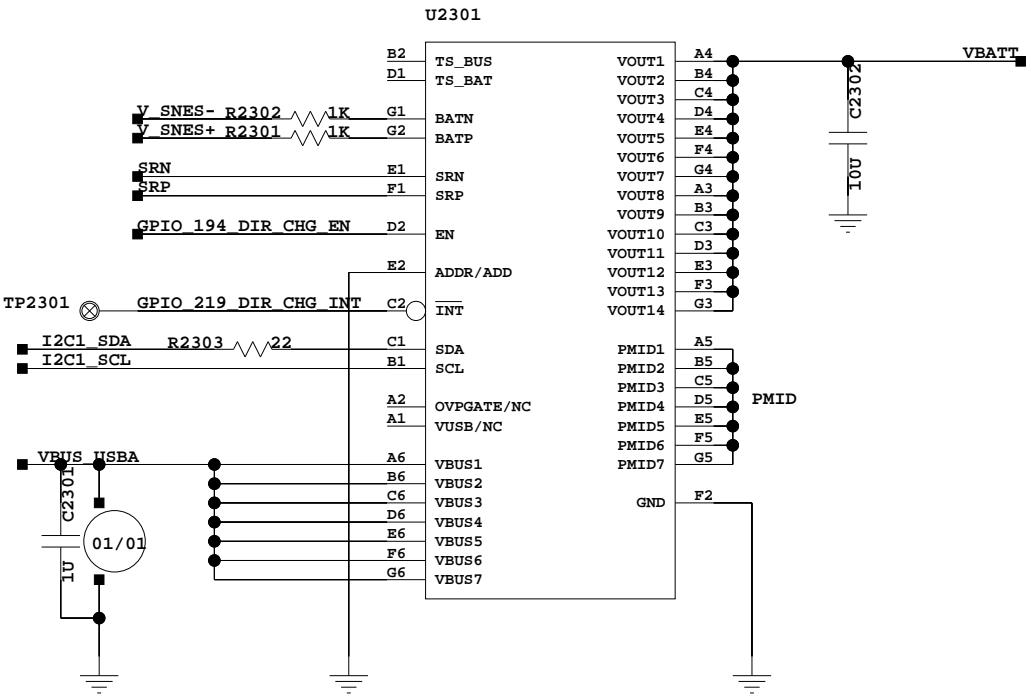
ヨウコ、ア



23. SC Charge

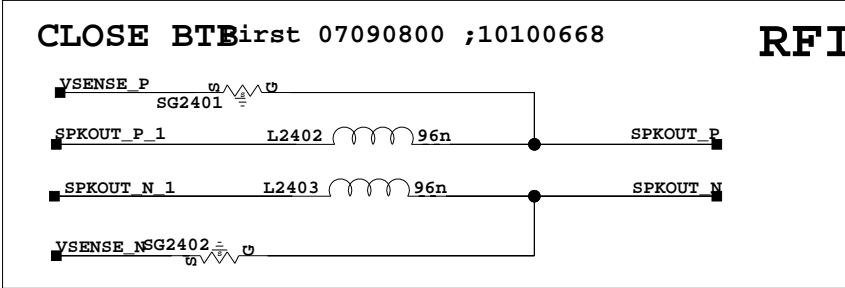
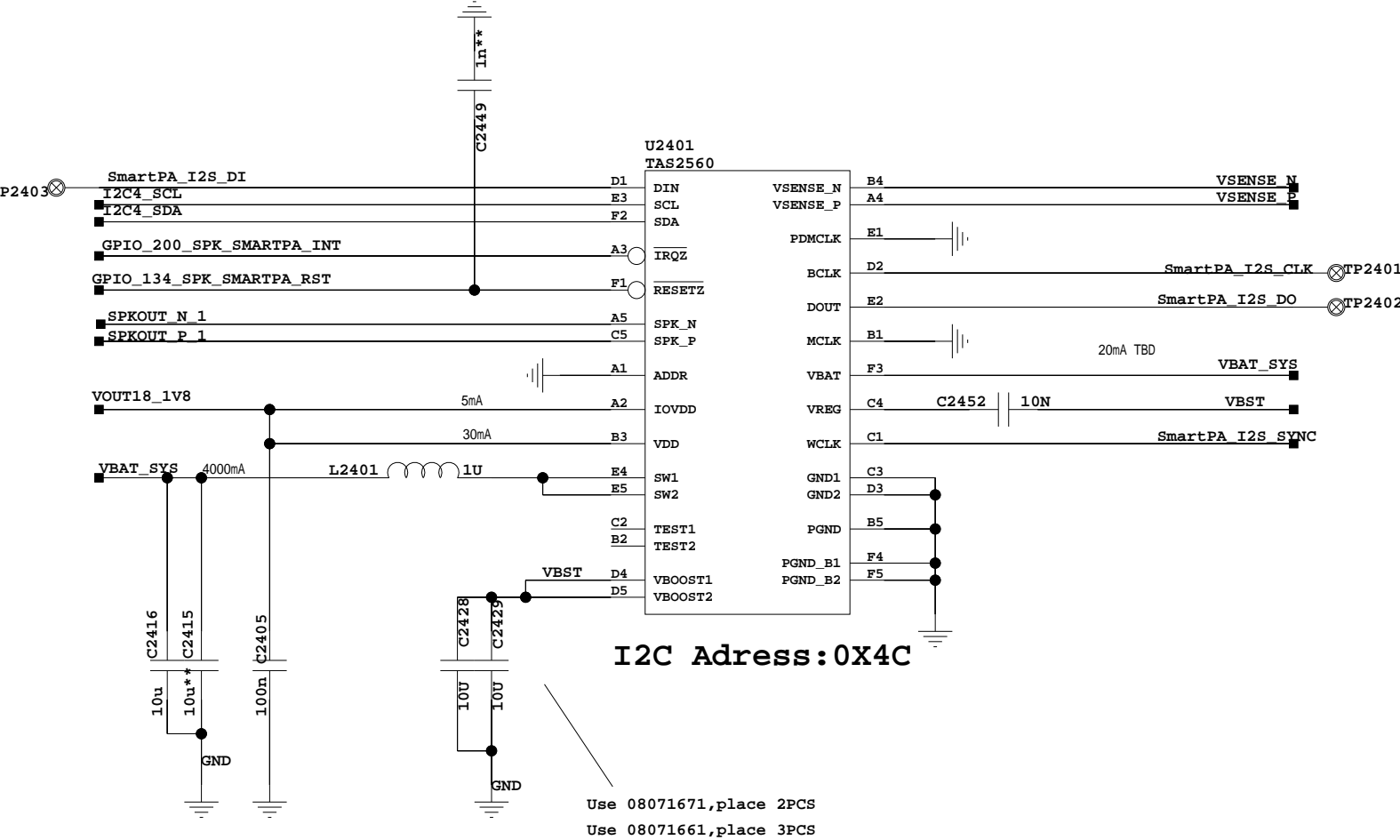
ארכיטקטורה

I2C Address=1100111(0x67)



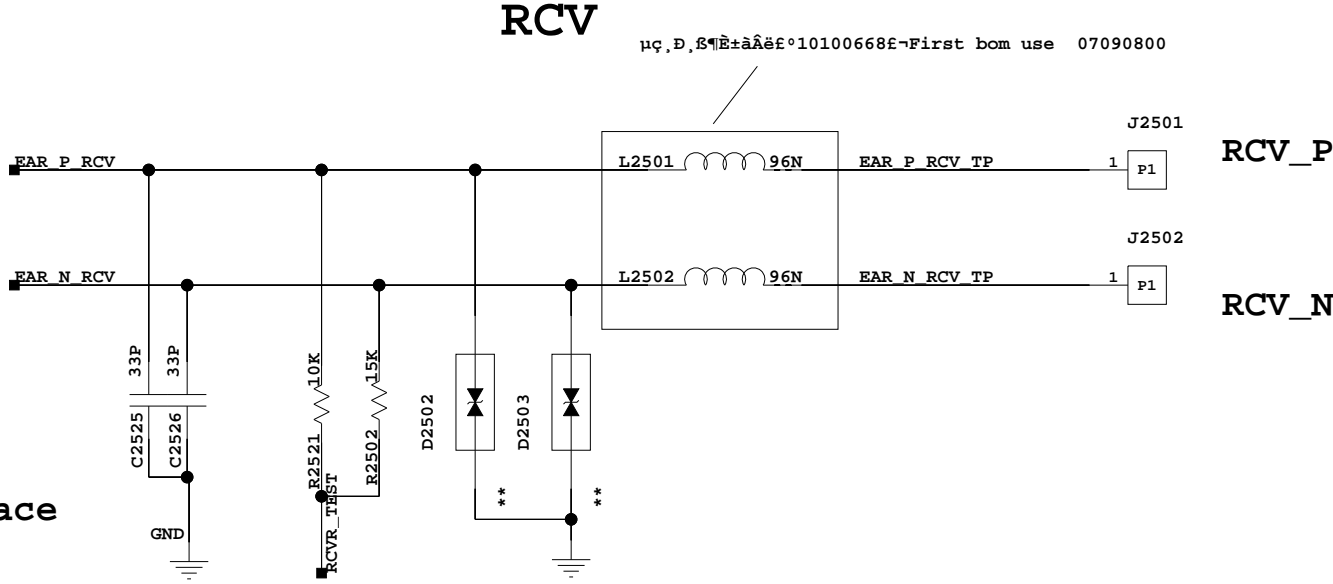
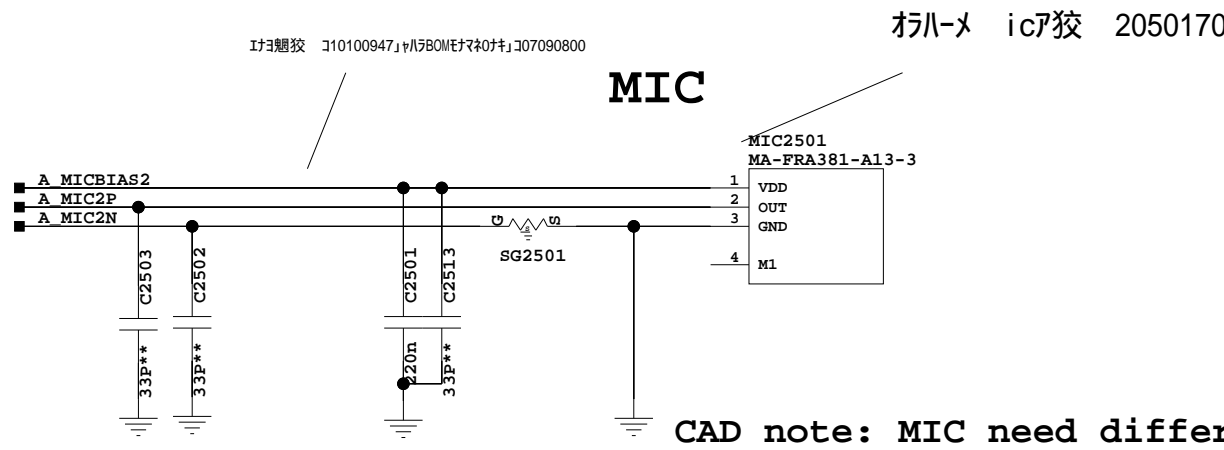
24. SPK SMART PA

1がフ惕



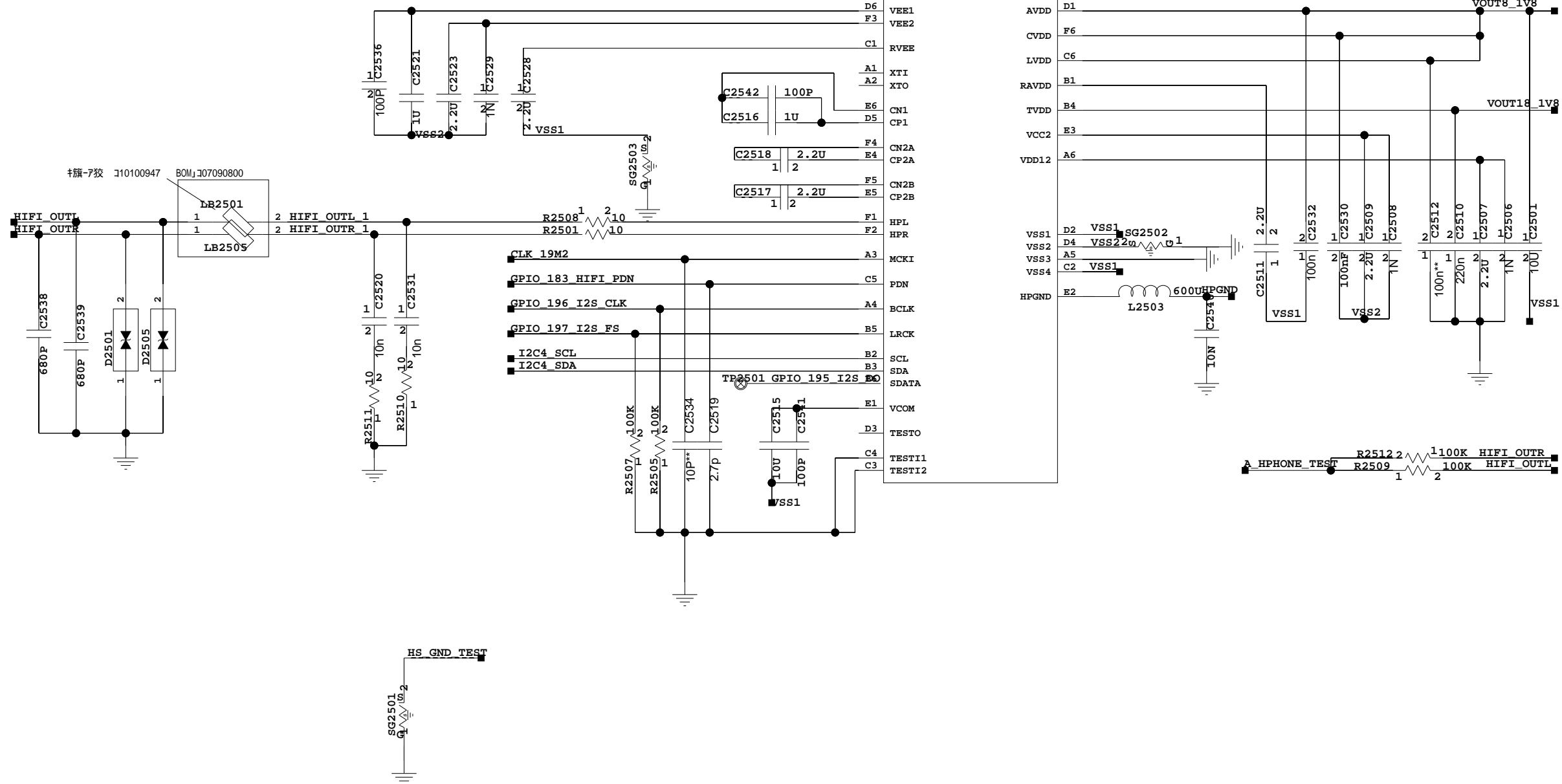
25. MIC/REC/HAC/HIFI

1つだけ

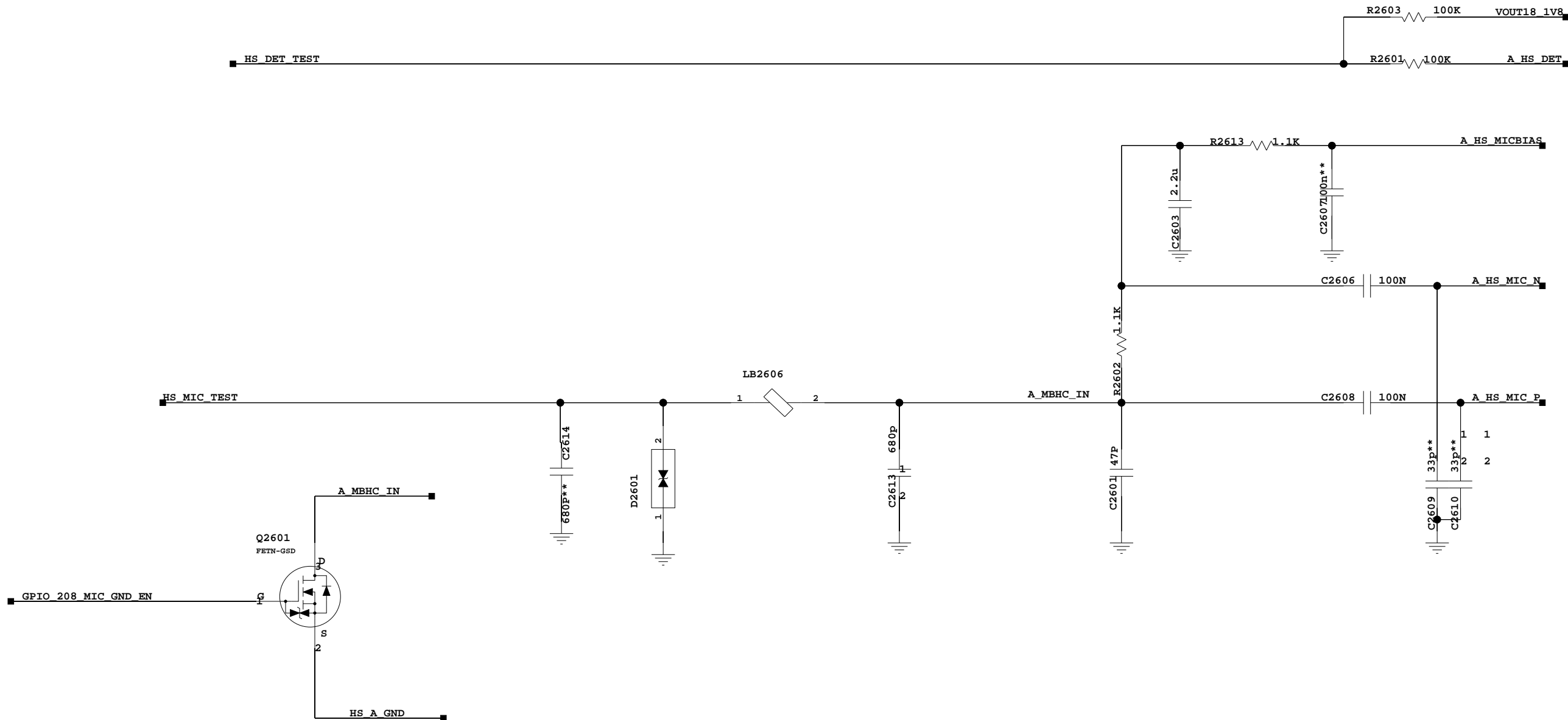
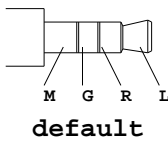


HIFI

U2501 ADDR LOW2C address=0010000



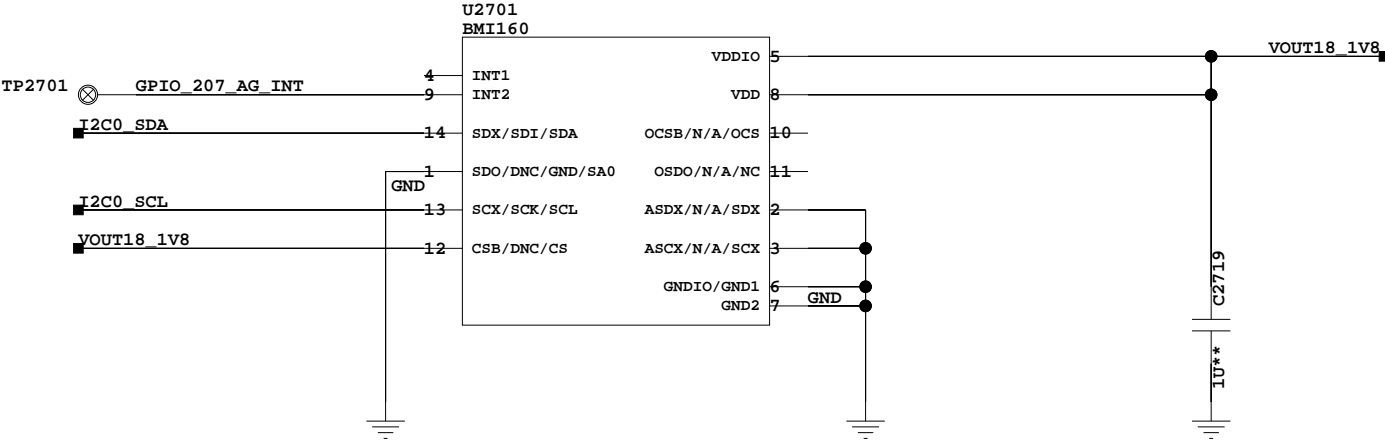
26. Headphone



27. X-Sensor

夕鏡入チ

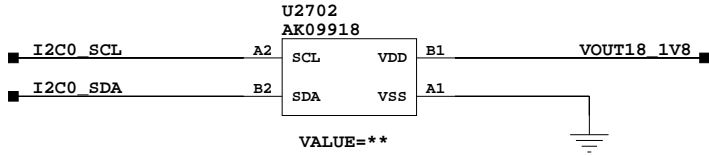
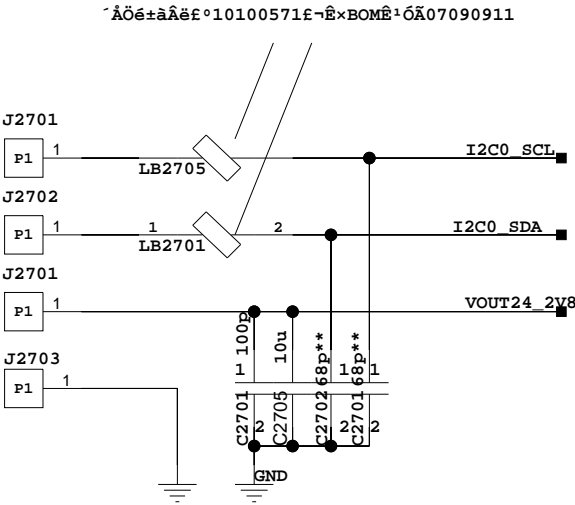
Accelerometer and Gyroscope 6-AXIS SENSOR



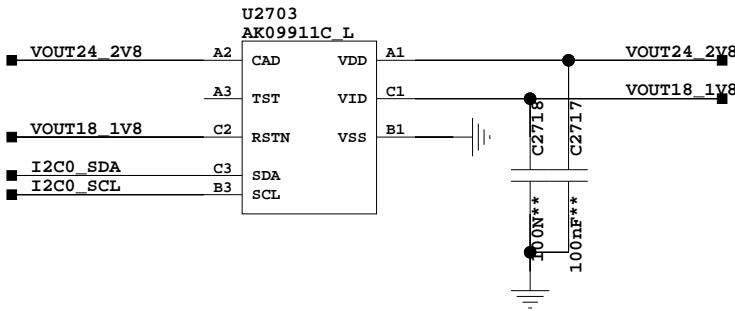
SDO=1 ADDRESS=0X6B(ST)/0X69(BOSCH)
SDO=0 ADDRESS=0X6A(ST)/0X68(BOSCH)

PROXIMITY&RGB AMBIENT

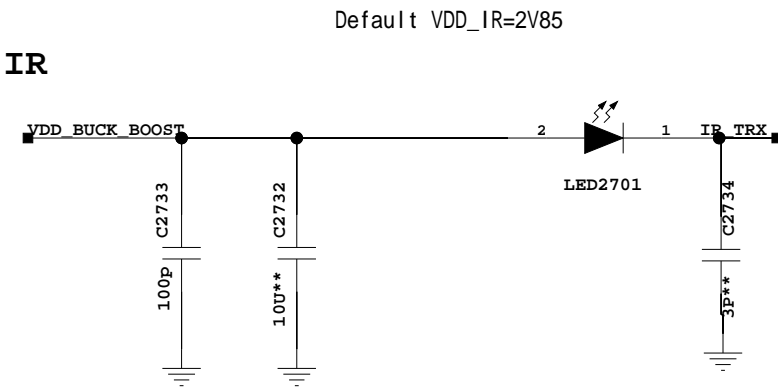
PROX ADDRESS=0X1E AMB ADDRESS=0X38



Compass

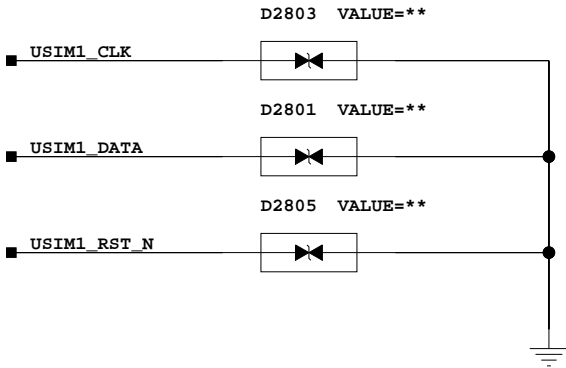
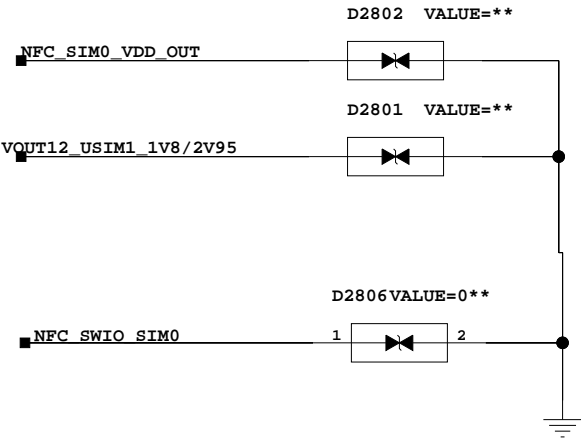
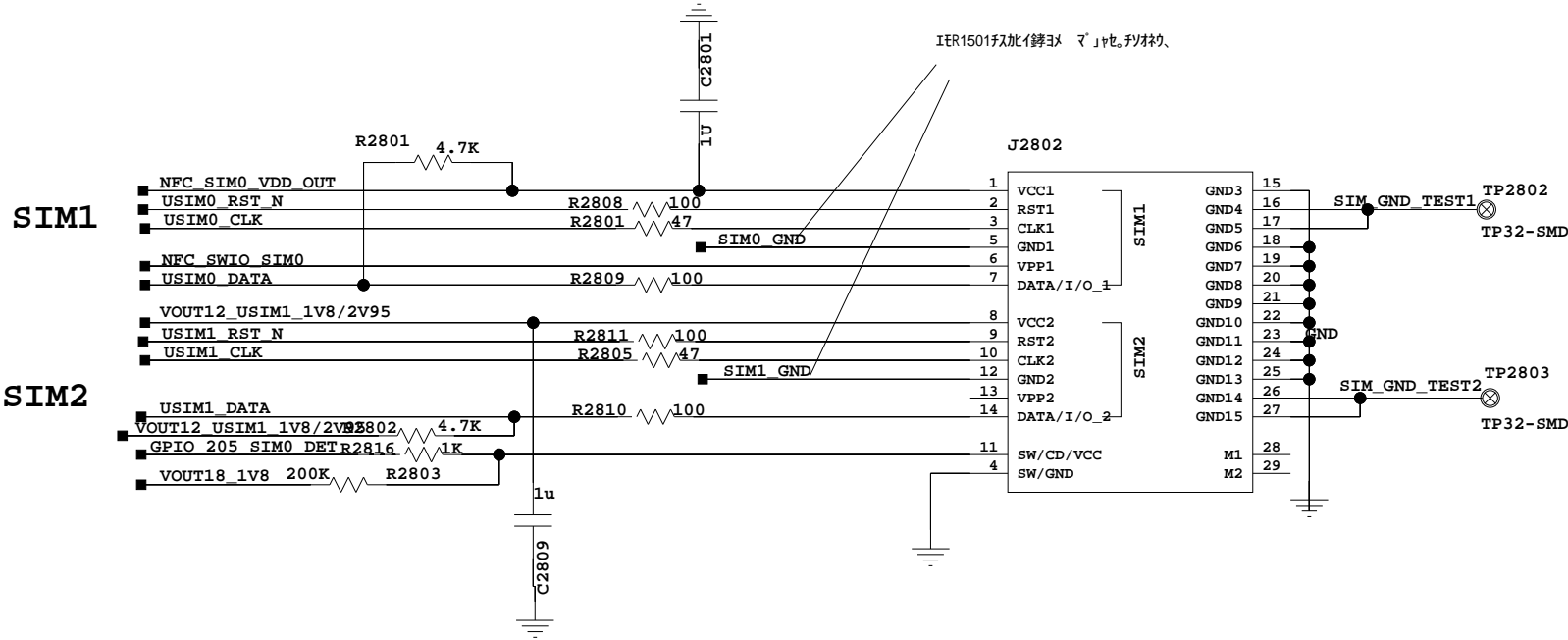


ADDRESS=0X0D



28.SIM/uSD Card

※「ネコ」

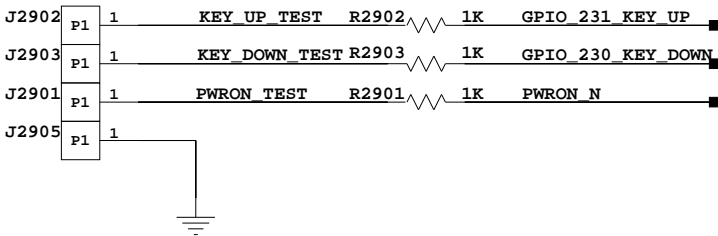


GPIO_202	SIM1_VCC	SE_VCC	VOUT12_2V95
	H		○
L		○	

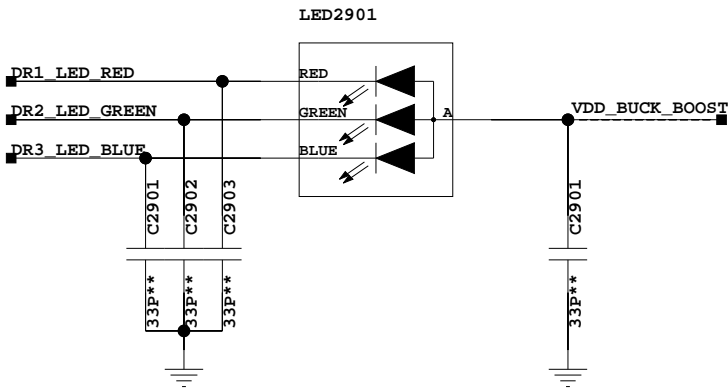
29. LED/FP/Key

※ 「ネ」

KEY SPRING



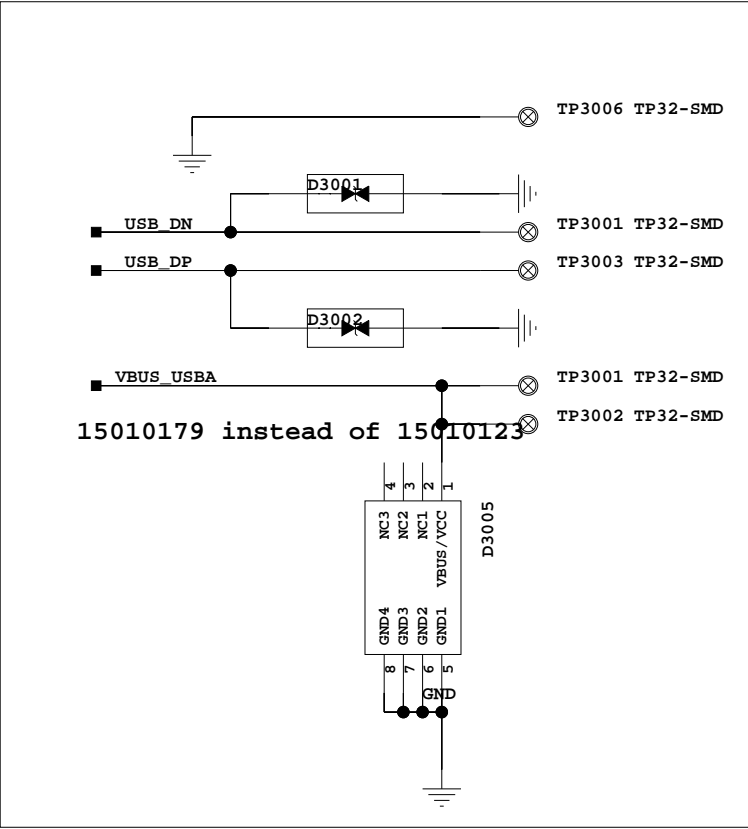
RGB LED



30. Test Points/Shields

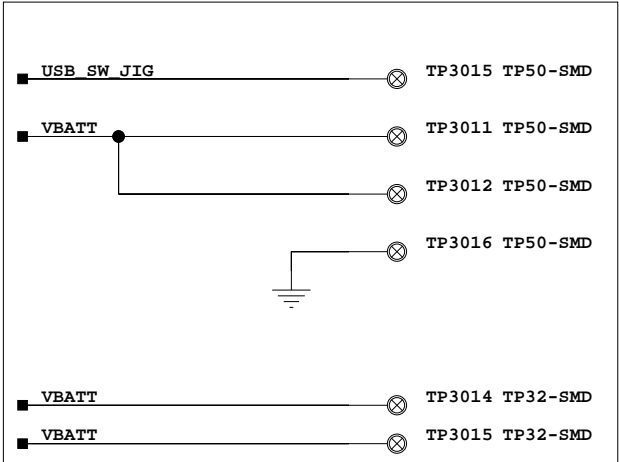
VBUS/VBATT/D+/D-/USB_SW_JIG_TEST/:2*TP50

TEST POINT for CT and BT

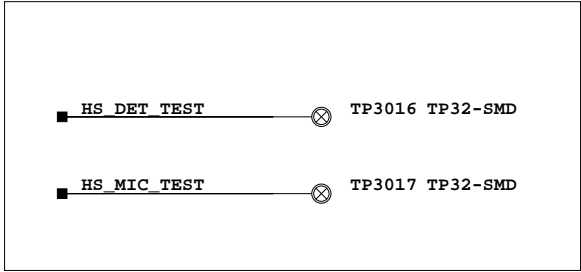


BOOT MODE R3001 1K TP3018 TP32-SMD

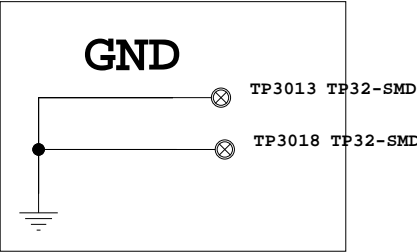
TEST POINT for PT



Headphone



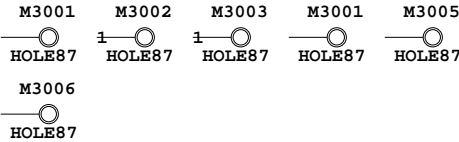
FUNCTION TEST



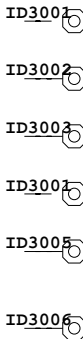
KEY

PWRON_TEST TP3035 TP32-SMD

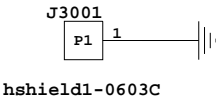
Hole



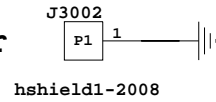
MARK POINT



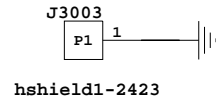
Tunner



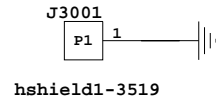
Charger



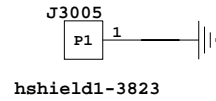
RF



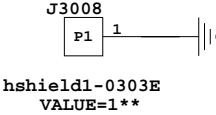
PMU



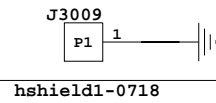
SOC



MIC

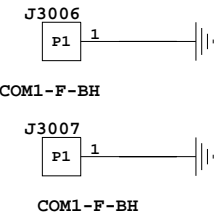


2 1 Çı



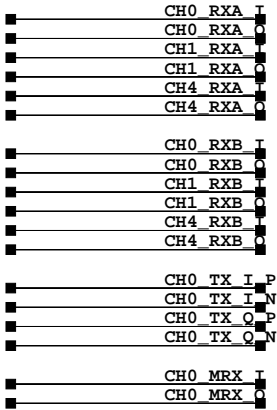
ニチアホリ

CAMメモリオッニヤ

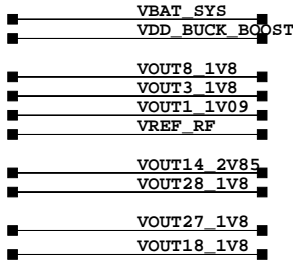


31.RF Interface

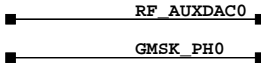
RFIC IQ



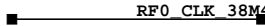
POWER



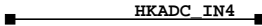
GSM



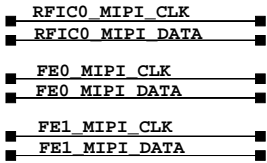
CLK



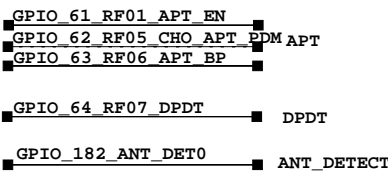
HKADC



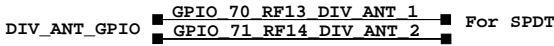
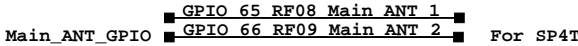
MIPI Interface



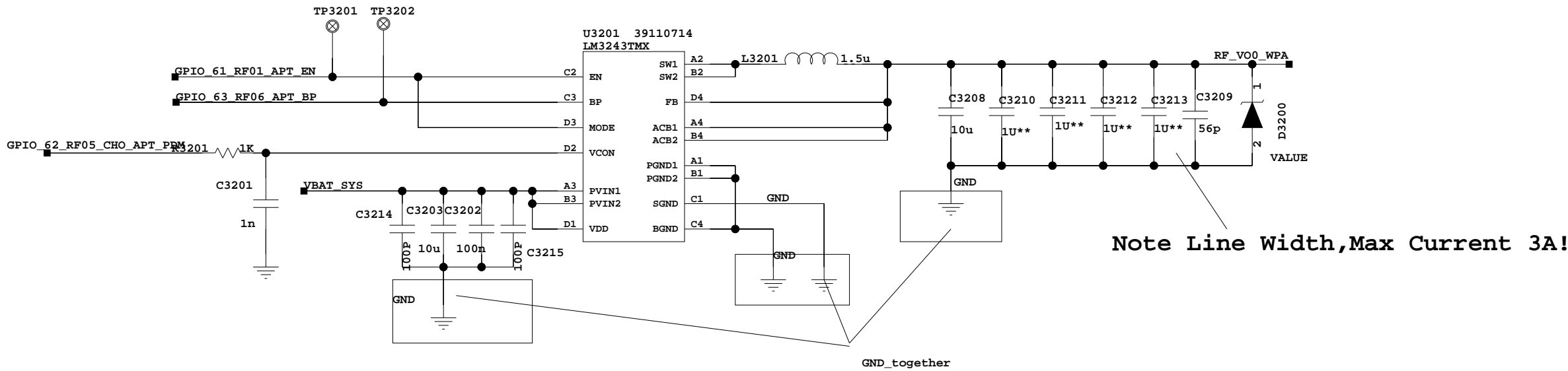
GPIO Interface



ANT Interface



32 APT_LM3243



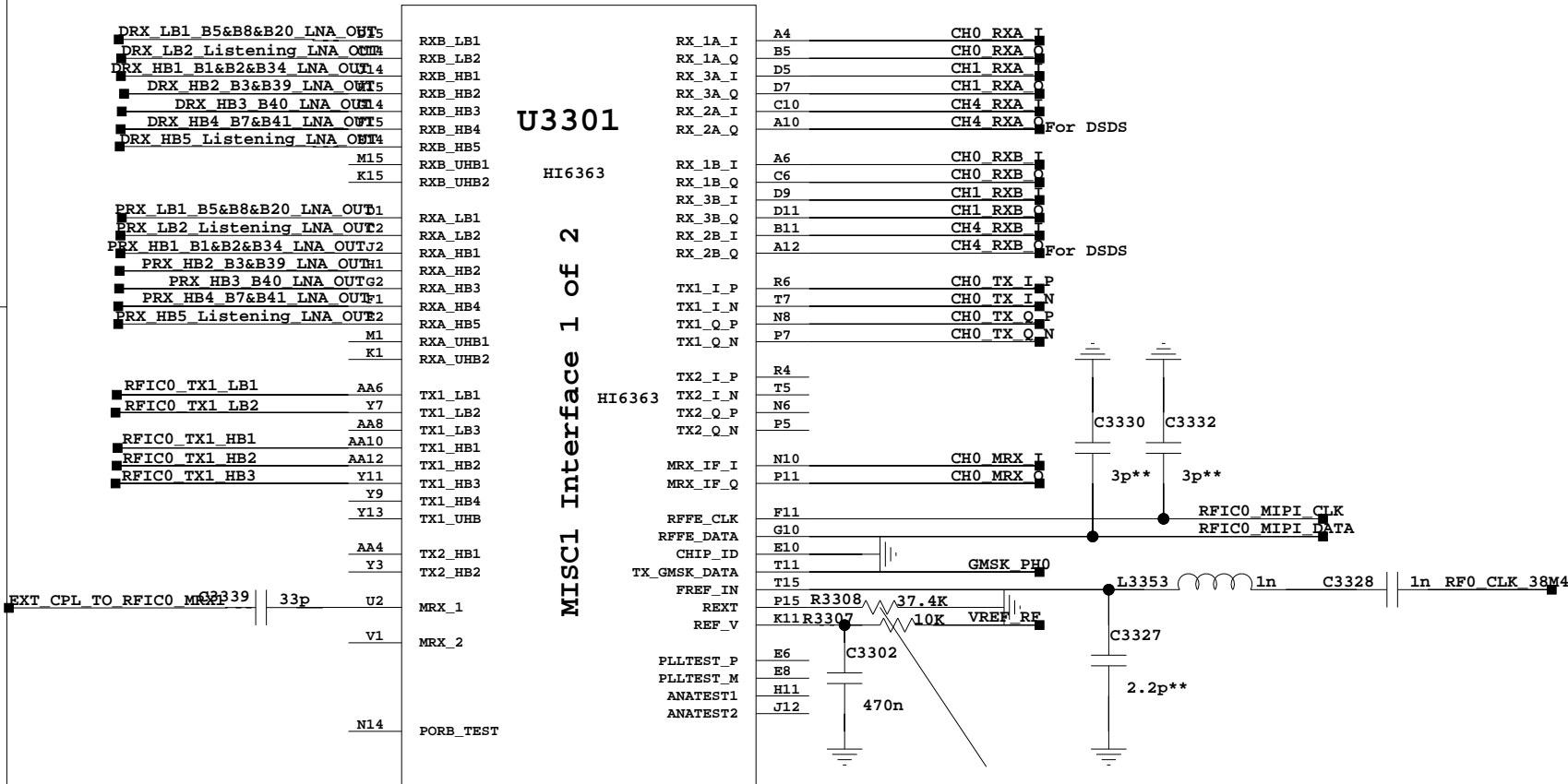
33.RFIC0_Hi6363

A

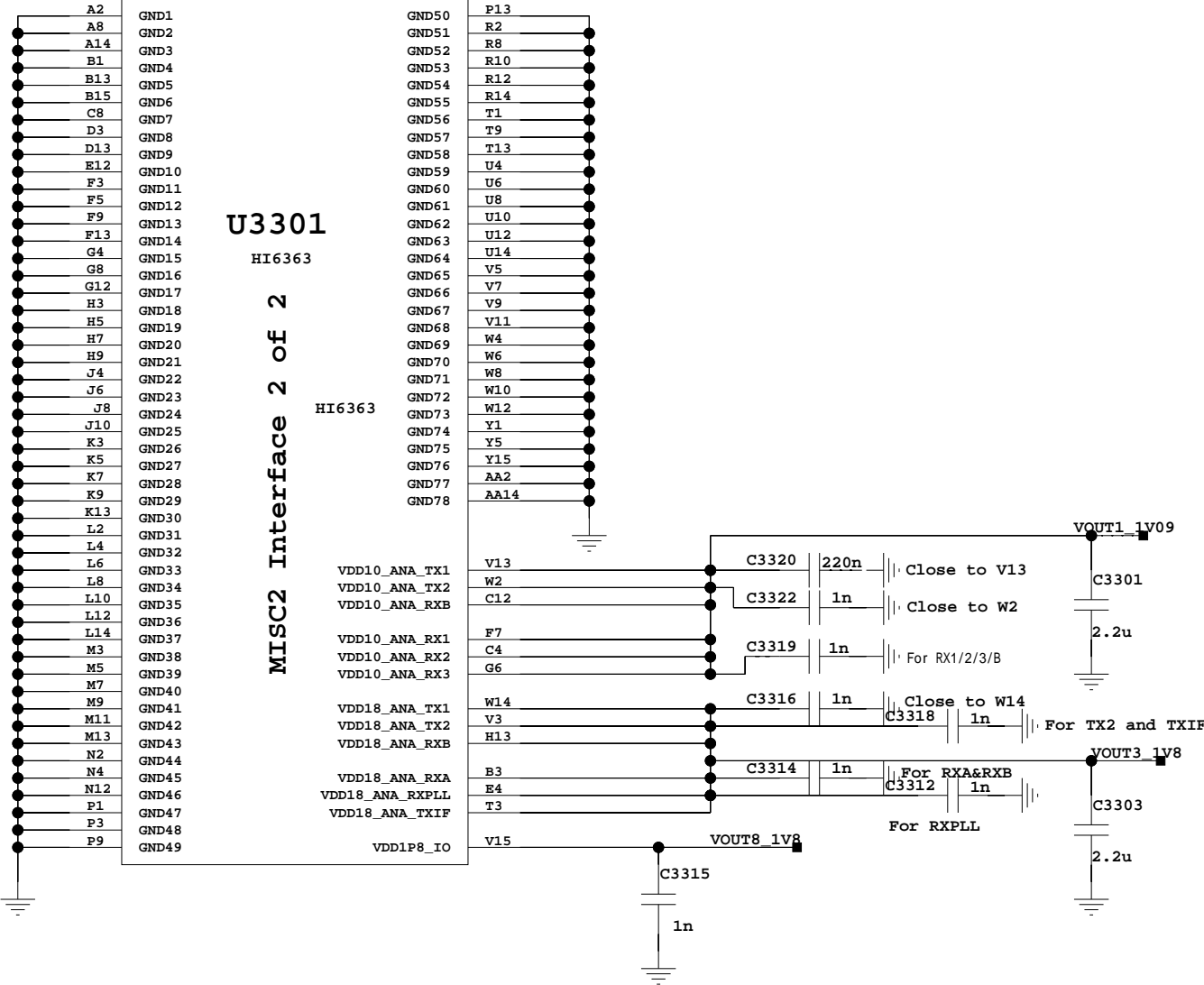
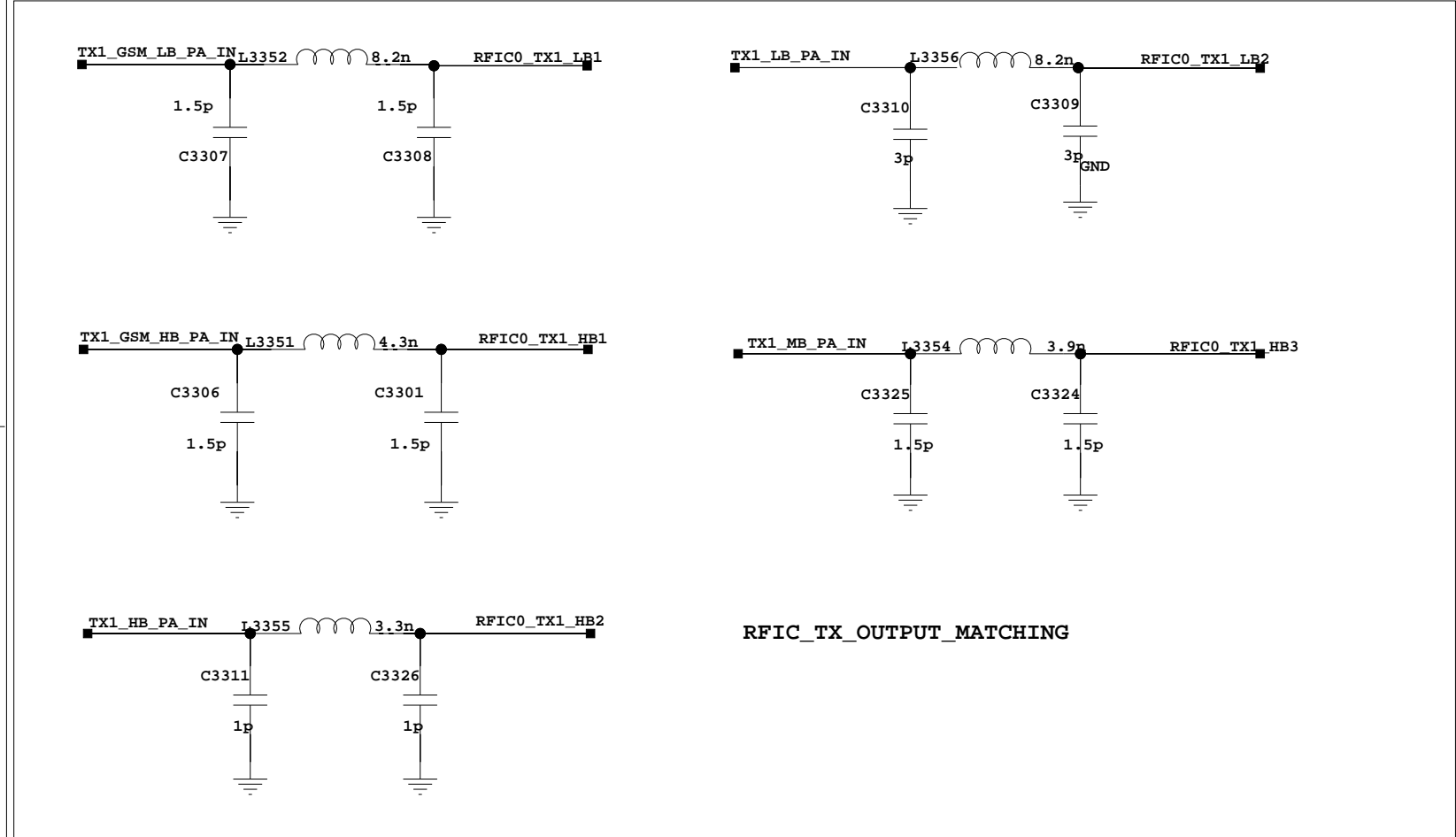
B

C

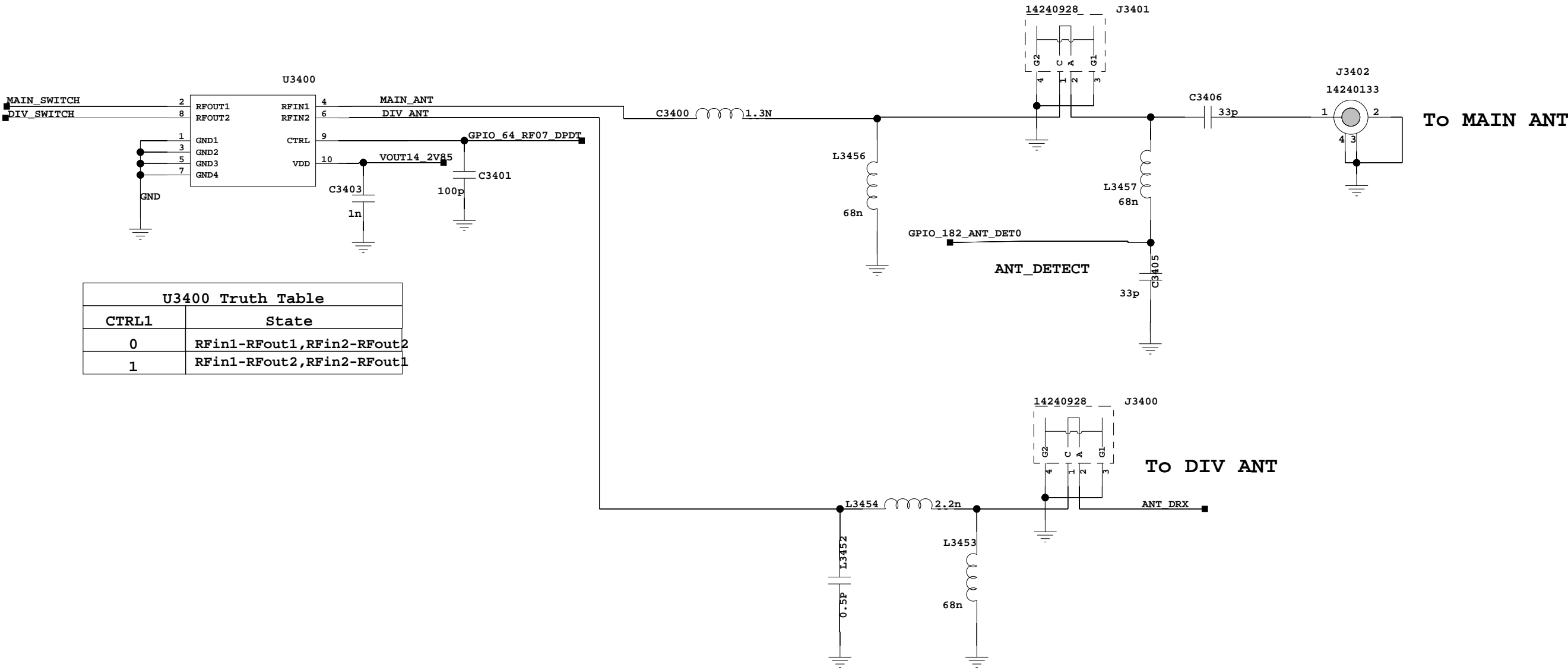
D



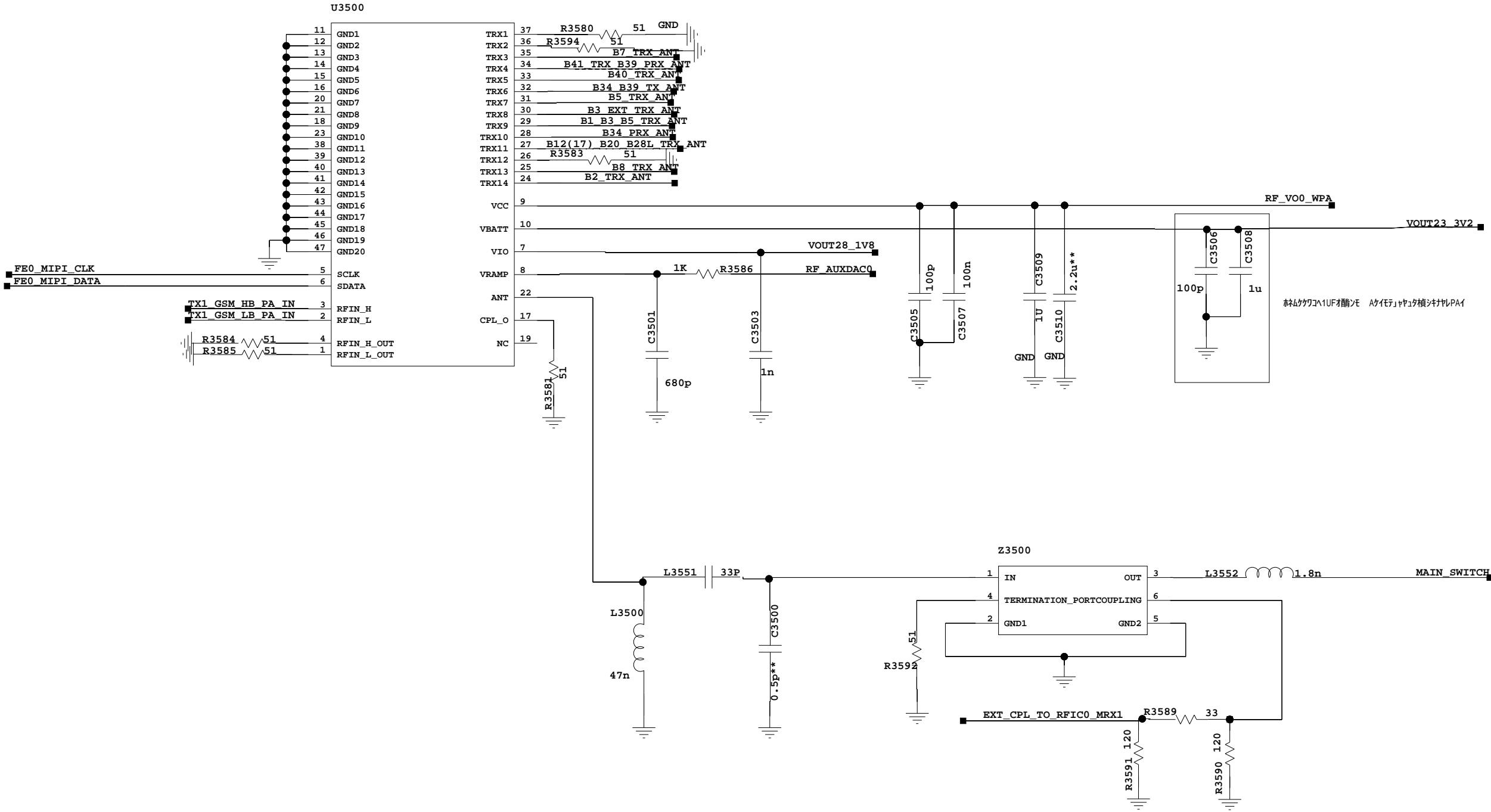
R3308 Need 37.4K , and 1%



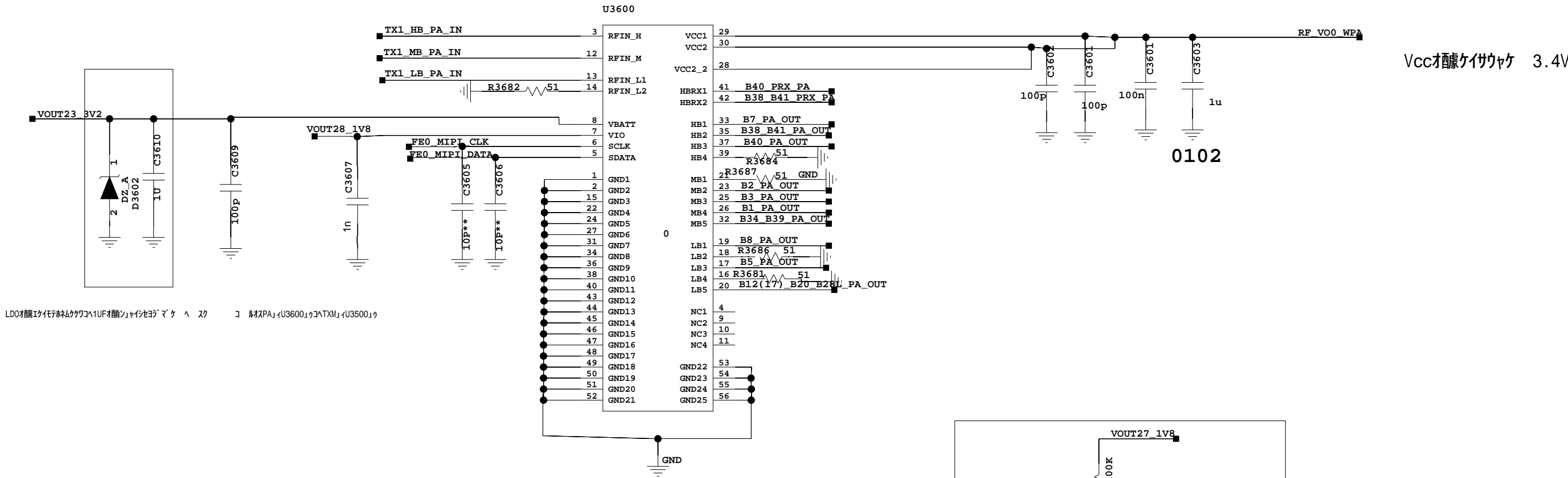
34 DPDT_Connector



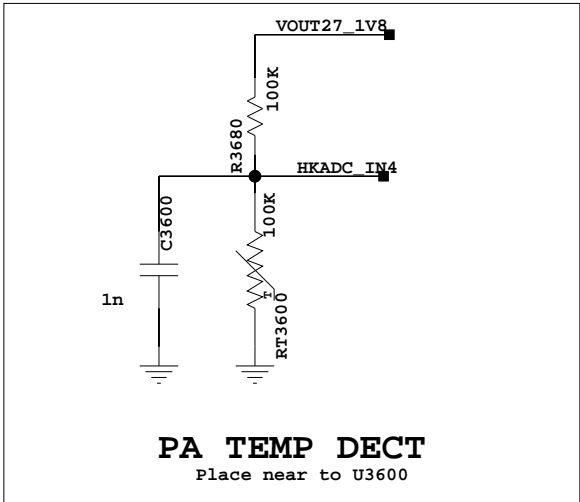
35. TXM



36 MMBPA

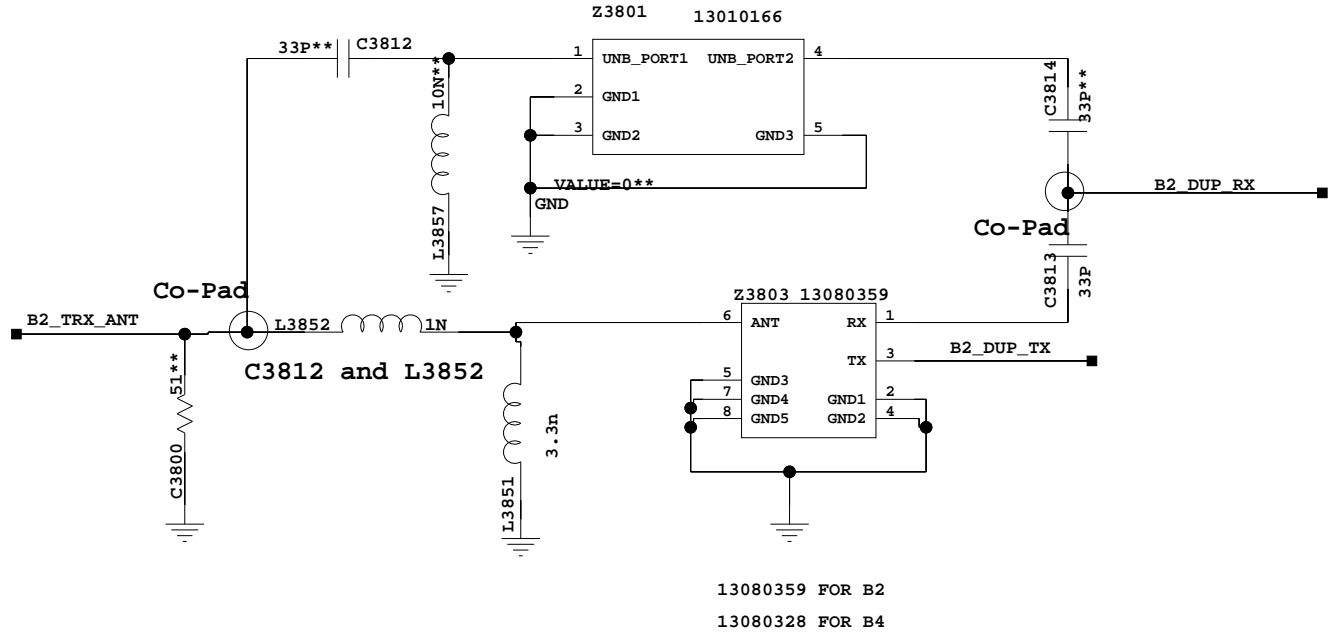
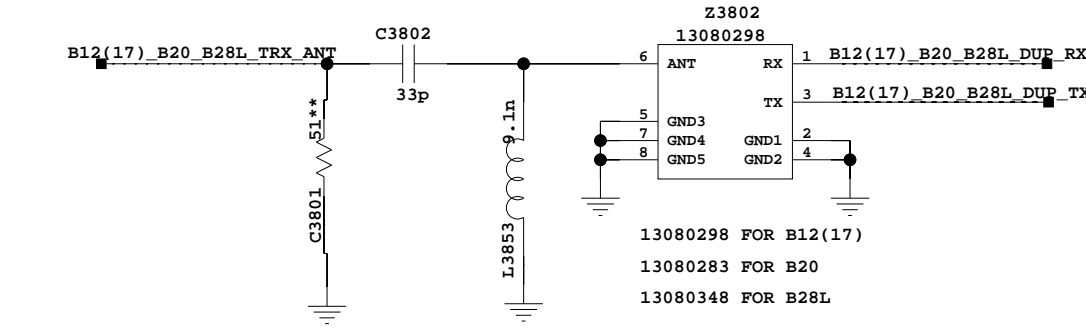
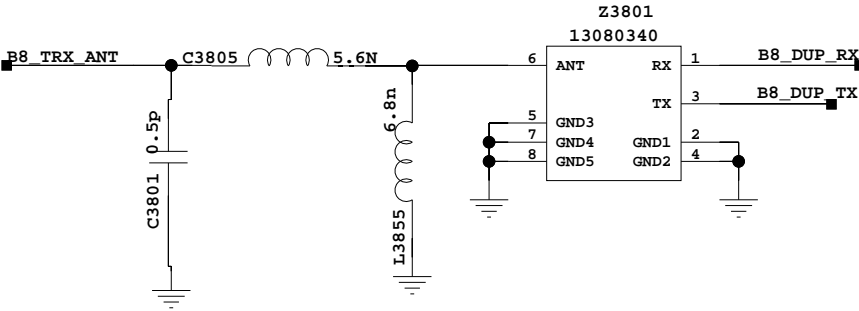
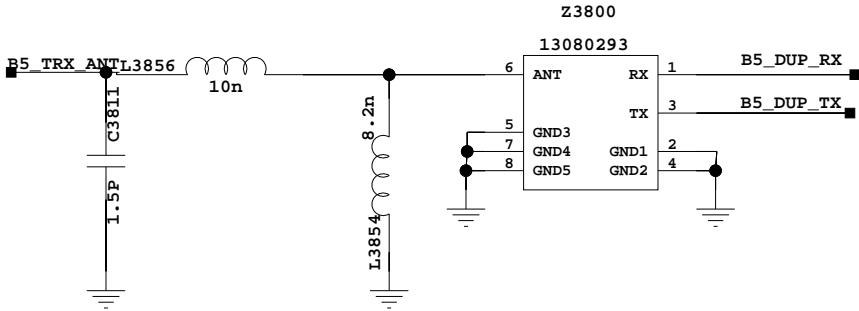


Vccオ醪ケイサウケ 3.4V



[illegible]

38 TRX_LB_1



39 TRX_LB_2					
1	2	3	4	5	6
A					A
B					B
C					C
D					D

40 TRX_MB_1

A

B

C

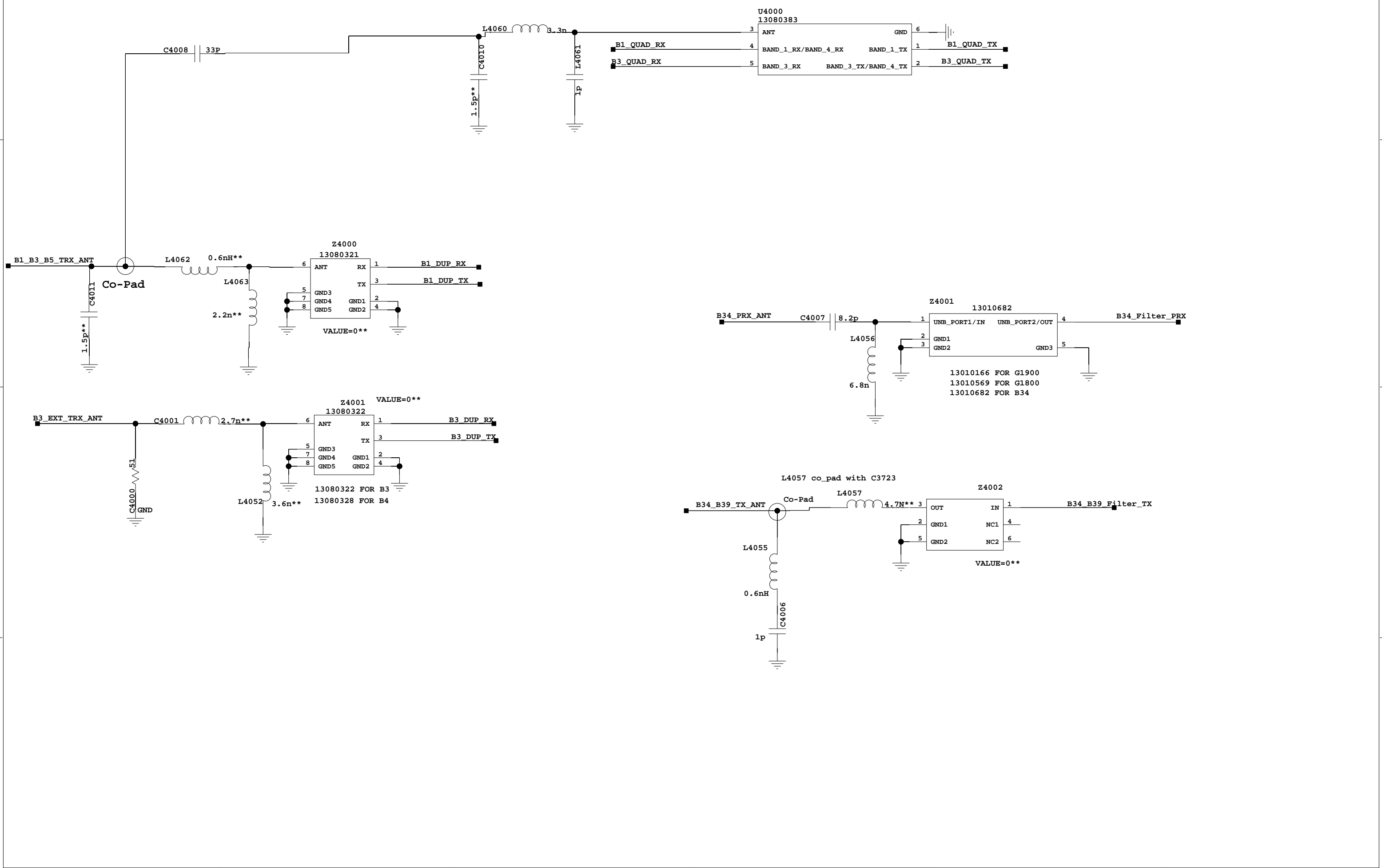
D

A

B

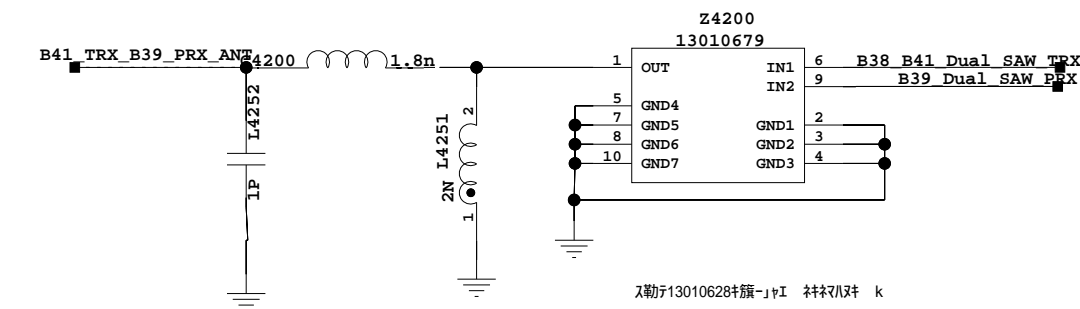
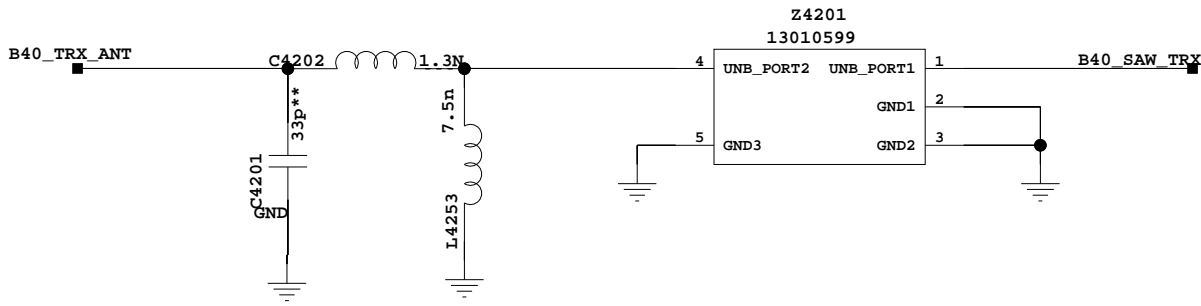
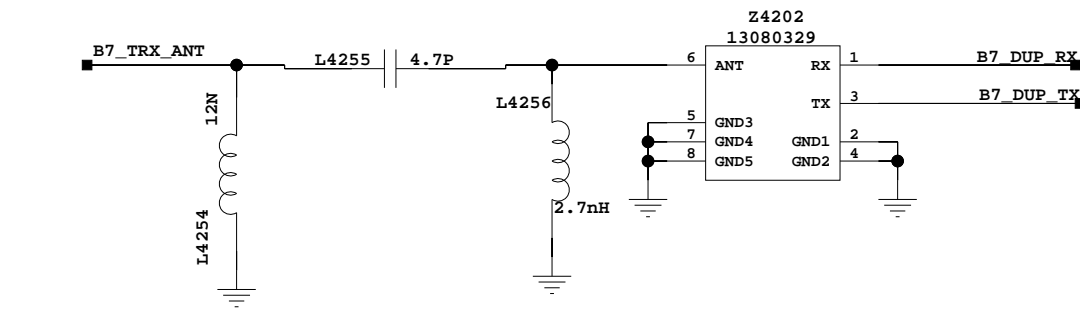
C

D



1	2	3	4	5	6
41 TRX_MB_2					
A					A
B					B
C					C
D					D
1	2	3	4	5	6

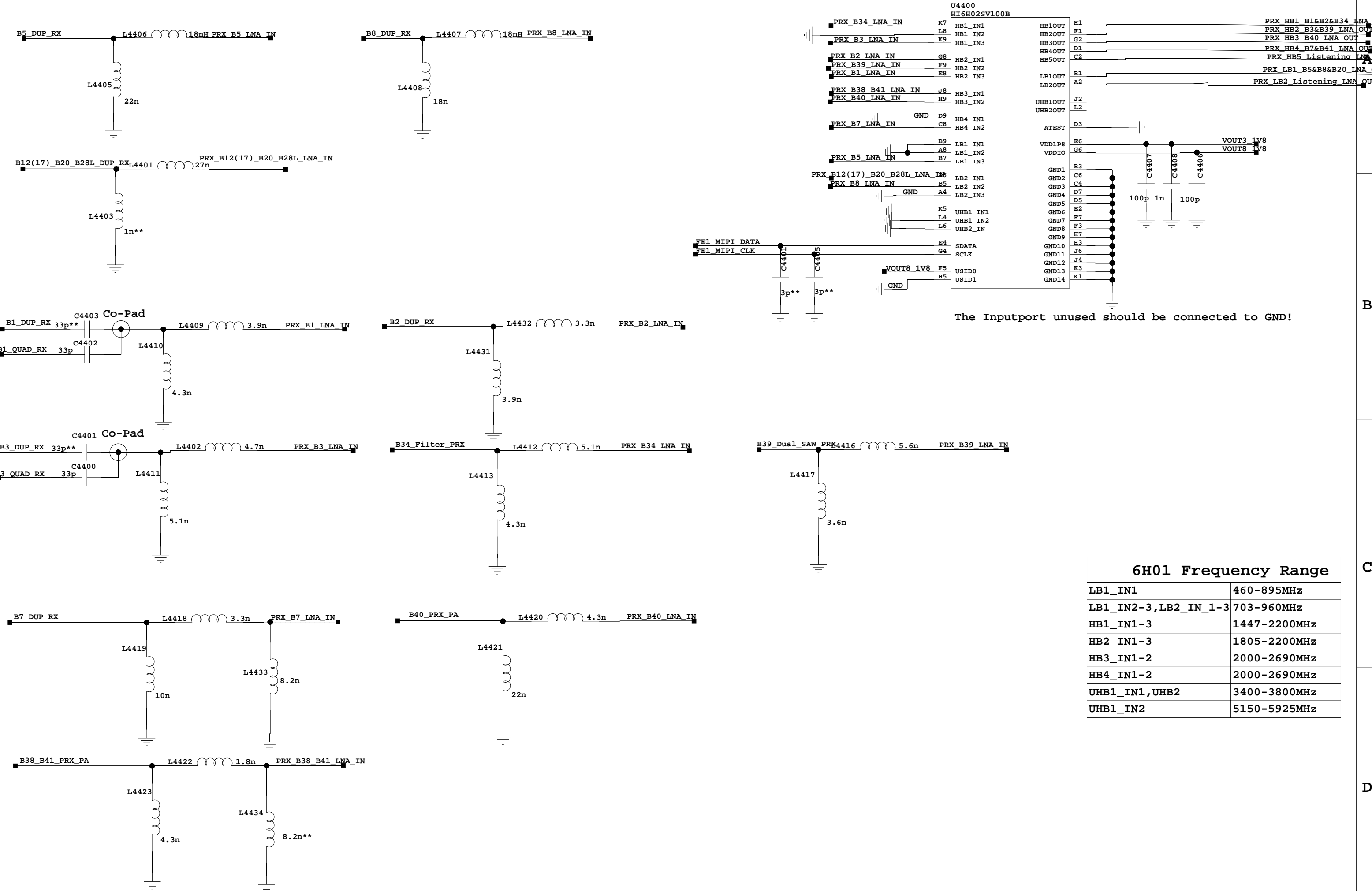
42 TRX_HB_1



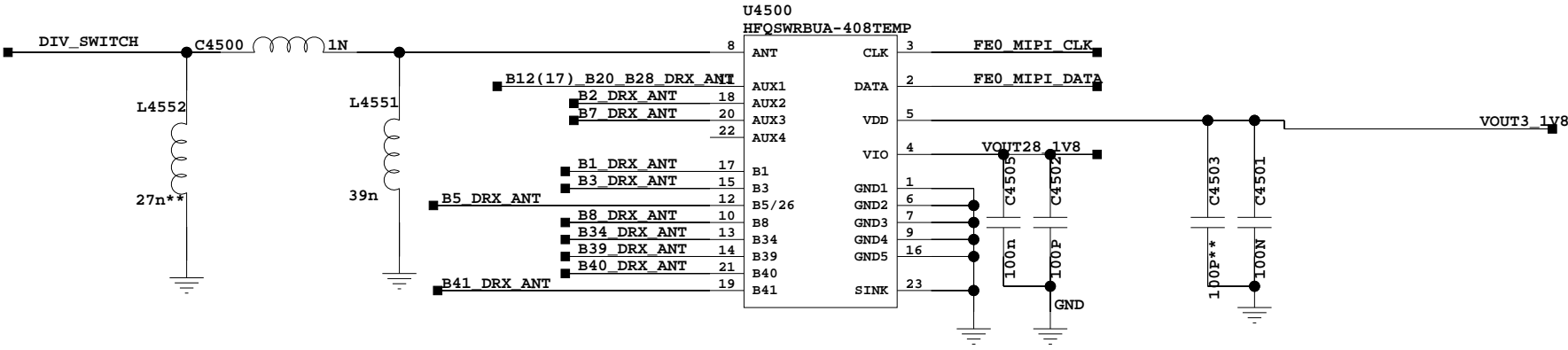
ス勒テ13010628针旗-JyI 々々々々 k

43. TRX_HB_2					
1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

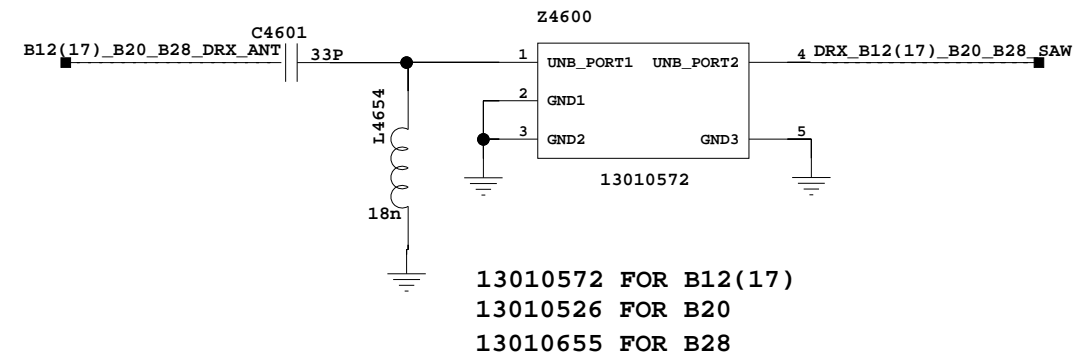
44. PRX_LNA_Module



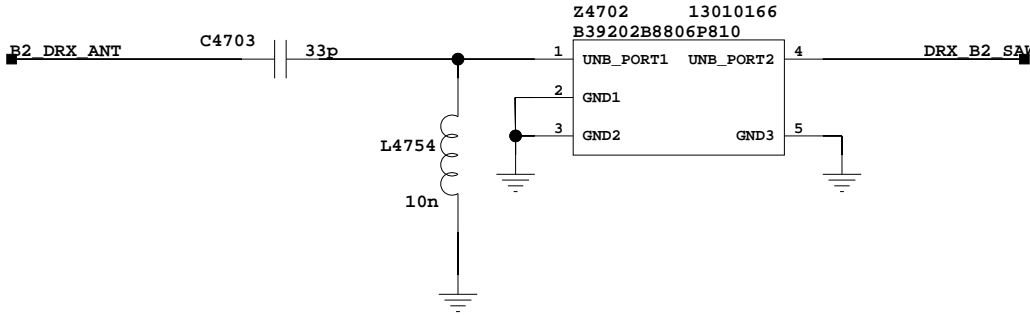
45.DRX_SWITCH



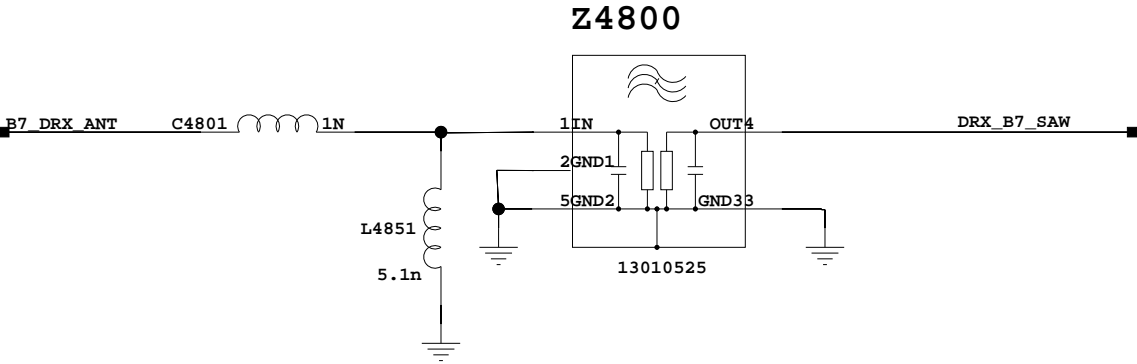
46. DRX_LB



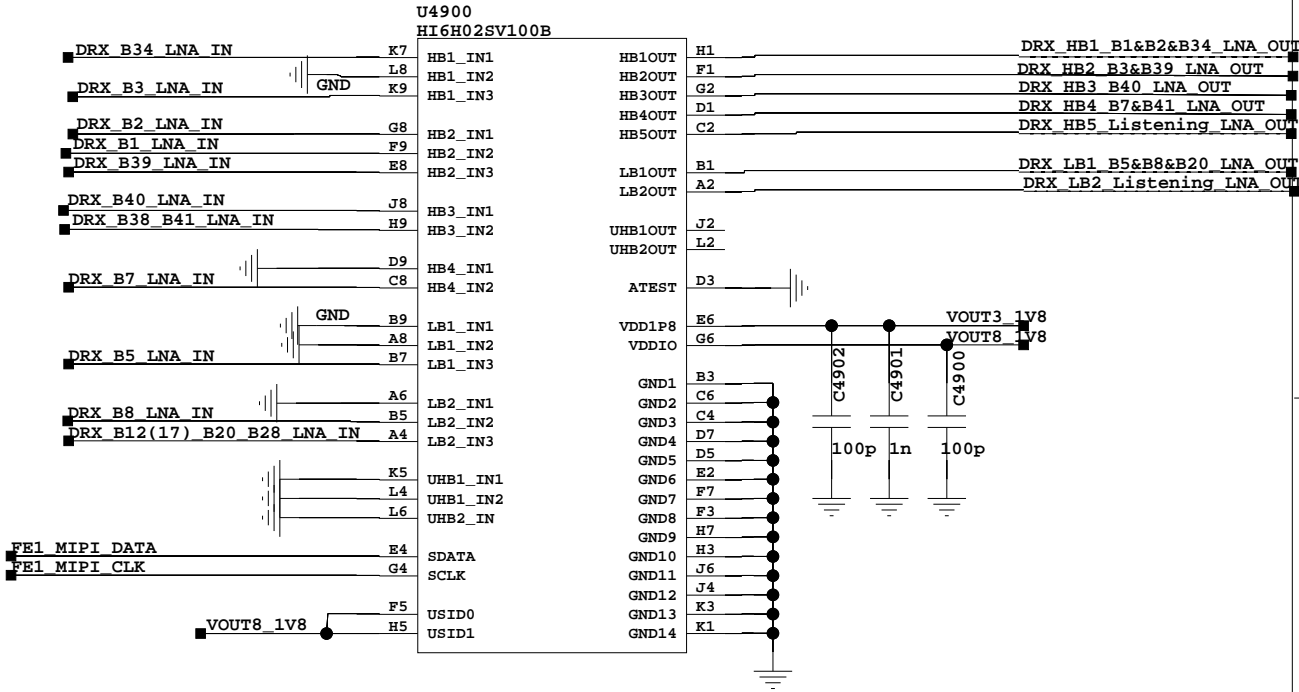
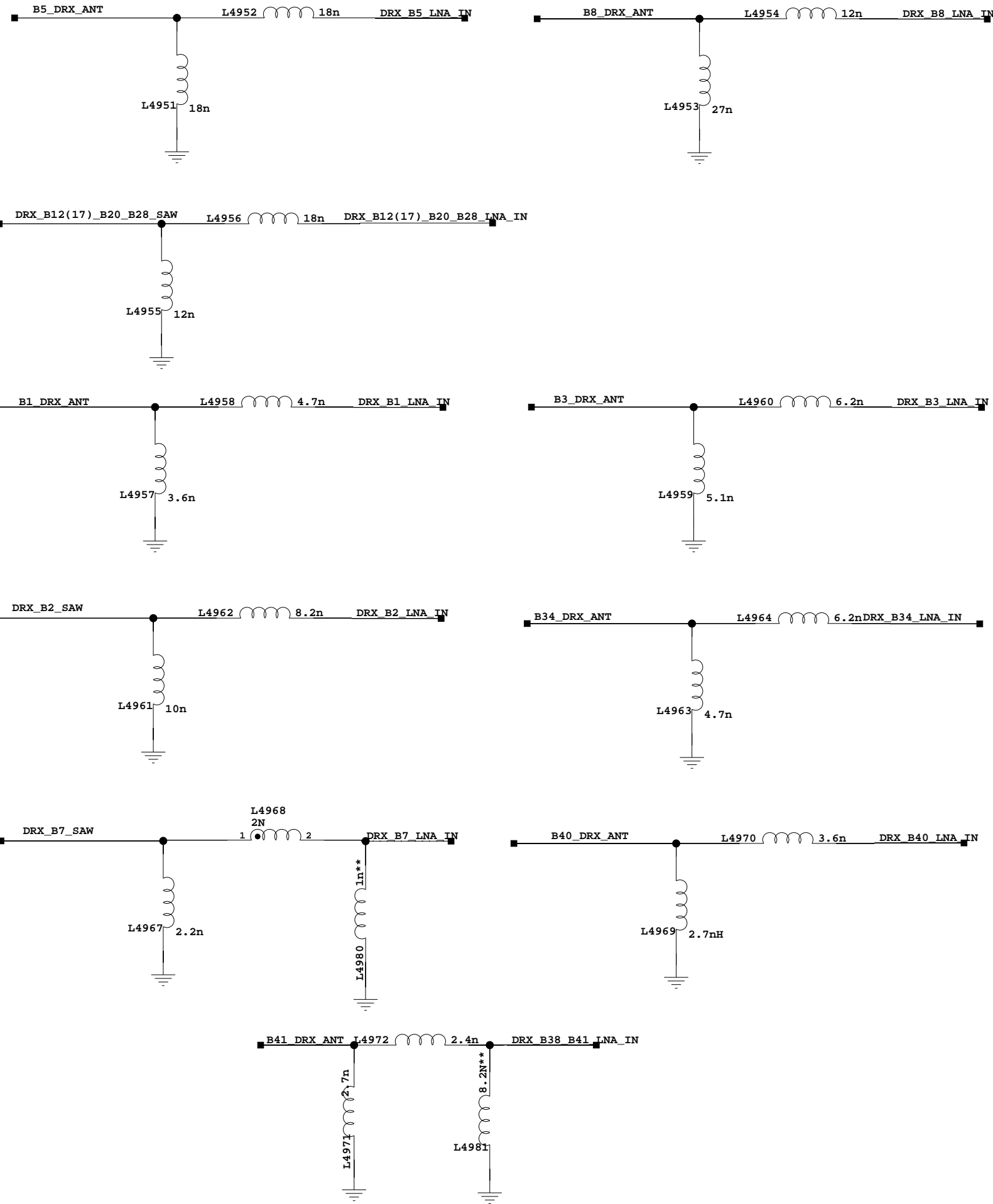
47. DRX_MB



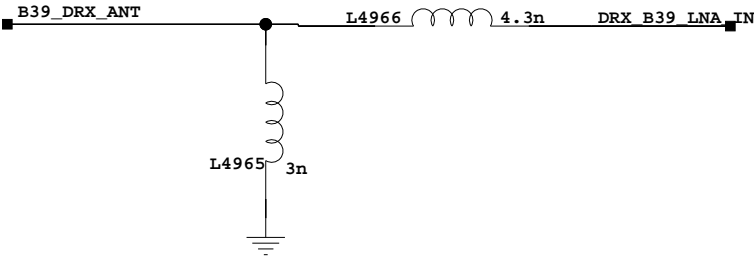
48. DRX_HB



49. DRX_LNA_Module



The Inputport unused should be connected to GND!



6H01 Frequency Range	
LB1_IN1	460-895MHz
LB1_IN2-3, LB2_IN_1-3	703-960MHz
HB1_IN1-3	1447-2200MHz
HB2_IN1-3	1805-2200MHz
HB3_IN1-2	2000-2690MHz
HB4_IN1-2	2000-2690MHz
UHB1_IN1, UHB2	3400-3800MHz
UHB1_IN2	5150-5925MHz

50. Reserved					
1	2	3	4	5	6
A					
B					
C					
D					

51. Reserved_RFIC1					
1	2	3	4	5	6
A					
B					
C					
D					
1	2	3	4	5	6

52. Reserved_APT1					
1	2	3	4	5	6
A					
B					
C					
D					

53. Reserved_MIMO_DRX2					
1	2	3	4	5	6
A					
B					
C					
D					
1	2	3	4	5	6

54. Reserved_DRX3					
1	2	3	4	5	6
A					
B					
C					
D					

55. Reserved_PA_SUB_6G					
1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

56. Reserved_TRX_SUB_6G					
1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

1

2

3

4

5

6

57. Reserved_DRX_SUB_6G

A

A

B

B

C

C

D

D

1

2

3

4

5

6

58.Reserved					
1	2	3	4	5	6
A					A
B					B
C					C
D					D

59. Reserved					
1	2	3	4	5	6
A					
B					
C					
D					

60 NC_POWER

U6001
HI1102

PWR Interface 2 of 2

NO IR-->J16: NC

NO 5G-->Delete D6002

NO IR--> Delete SG6005

VDD PMU 1P4

VDD PMU CLDO1

VDD PMU RFLDO1

VDD CMU LDO TCXO

VDD PMU RFLDO1

VBAT SYS

VDD PMU 1P4

VDD PMU CLDO1

VDD PMU PALDO

VDD PMU RFLDO1

VDD PMU RFLDO2

VOUT18 1V8

VOUT18 1V8

VBAT SYS

VDD PMU RFLDO1

VDD PMU RFLDO1

VDD PMU RFLDO1

VDD PMU PALDO

VDD PMU PALDO

VDD PMU RFLDO1

VDD PMU RFLDO1

VDD PMU RFLDO2

VOUT18 1V8

VDD_BUCK_1P4

VDD_CLDO1_1

VDD_CLDO1_2

VDD_CLDO1_3

VDD_CLDO1_4

VDD_CMU_1P2

VDD_CMU_LDO_TCXO

VDD_FM_RF_RX_1P2

VDD_GF_RF_PLL_VCO_1P2

VDD_GNSS_RF_RX_1P2

VDD_NFC_PMU_SE1

VDD_NFC_PMU_SE2

VDD_NFC_PMU_SWIO0

VDD_NFC_PMU_SWIO1

VDD_NFC_PMU_SWIO2

VDD_NFC_RF_PA

VDD_NFC_RF_REC

VDD_NFC_VBAT

VDD_PMU_1P4

VDD_PMU_CLDO1

VDD_PMU_CLDO2

VDD_PMU_PALDO

VDD_PMU_RFLDO1

VDD_PMU_RFLDO2

VDD_PMU_SYSLDO

VDD_PMU_VBAT2

VDD_PMU_VDDIO

VDD_PMU_VDDIO2

VDD_PMU_VPH

VDD_WB_ADDA_1P2

VDD_WB_RF_LNA_1P2

VDD_WB_RF_LPF_1P2

VDD_WB_RF_PA2G_3P3_1P4

VDD_WB_RF_PPA5G_3P3

VDD_WB_RF_PLL_1P2

VDD_WB_RF_UPC2G5G_1P2

VDD_WB_RF_VCO_LOGEN_1P2

VDDIO

VSS_CMU

VSS_FM_RF_RX

VSS_GF_ADC

VSS_GF_RF_FMO_VCO

VSS_GF_RF_GR

VSS_GF_RF_PLL

VSS_GLOBAL_RF1

VSS_GLOBAL_RF2

VSS_GLOBAL_RF3

VSS_GLOBAL1

VSS_GLOBAL2

VSS_GLOBAL3

VSS_GLOBAL4

VSS_GLOBAL5

VSS_GNSS_RF_RX

VSS_IR

VSS_NFC_RF_ANA1

VSS_NFC_RF_ANA2

VSS_NFC_RF_GR

VSS_NFC_RF_PA

VSS_PMU_AGND1

VSS_PMU_AGND2

VSS_PMU_AGND3

VSS_PMU_PGND

VSS_PMU_PGND_TEST

VSS_SR1

VSS_SR2

VSS_WB_ABB_ADC_DAC

VSS_WB_RF_GR

VSS_WB_RF_LNA

VSS_WB_RF_MIX_UPC2G

VSS_WB_RF_PA2G_A

VSS_WB_RF_PA2G_B

VSS_WB_RF_PA2G_BL1

VSS_WB_RF_PA2G_BL2

VSS_WB_RF_PLL

VSS_WB_RF_PPA5G

VSS_WB_RF_PPA5G_BL

VSS_WB_RF_TRAP

VSS_WB_RF_TX5G_MIXBUF

VSS_WB_RF_VCO_LOGEN

SG6001

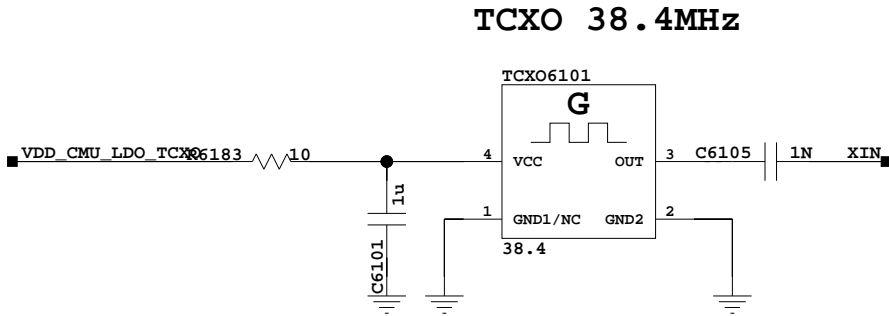
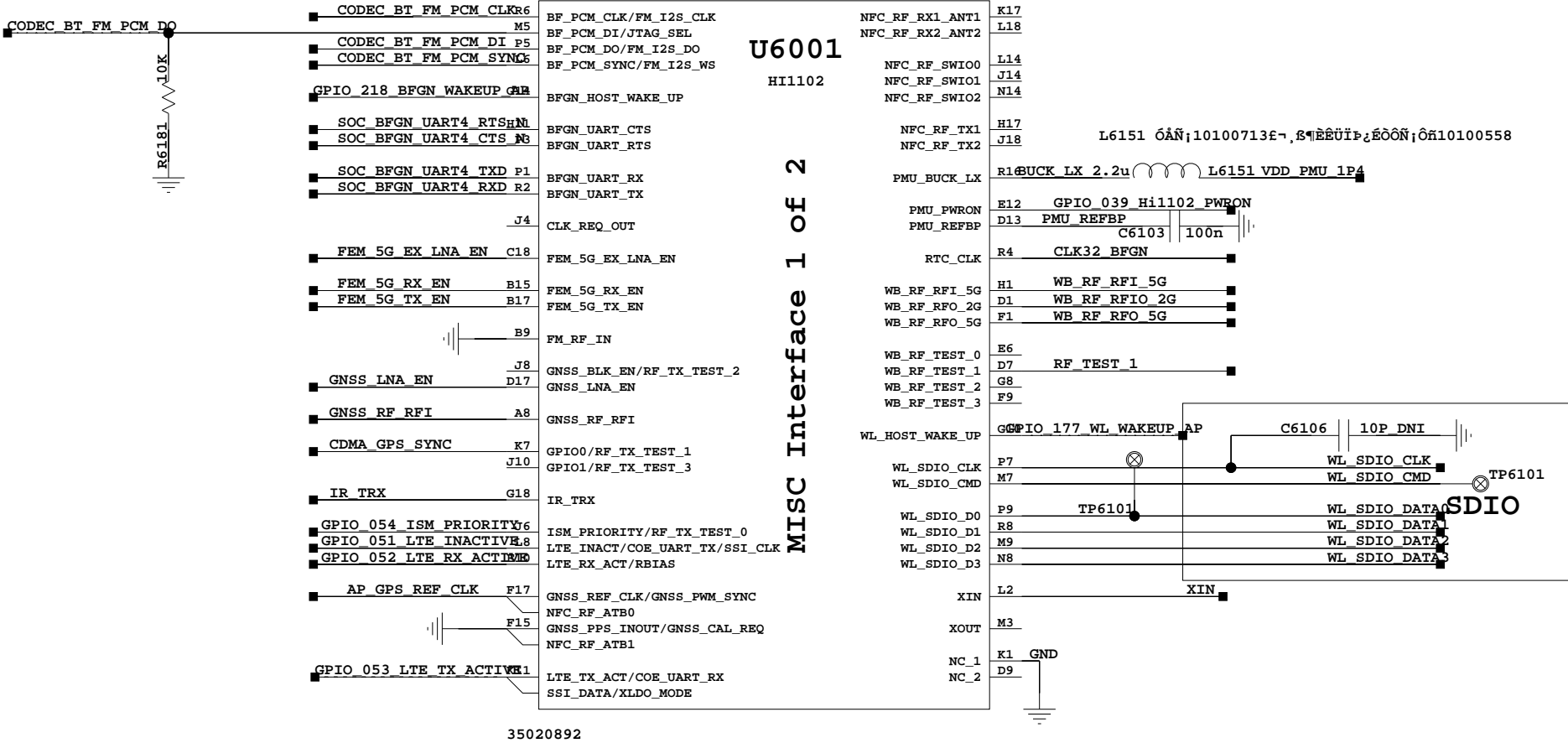
SG6002

SG6003

SG6005

SG6001

61 NC_BB

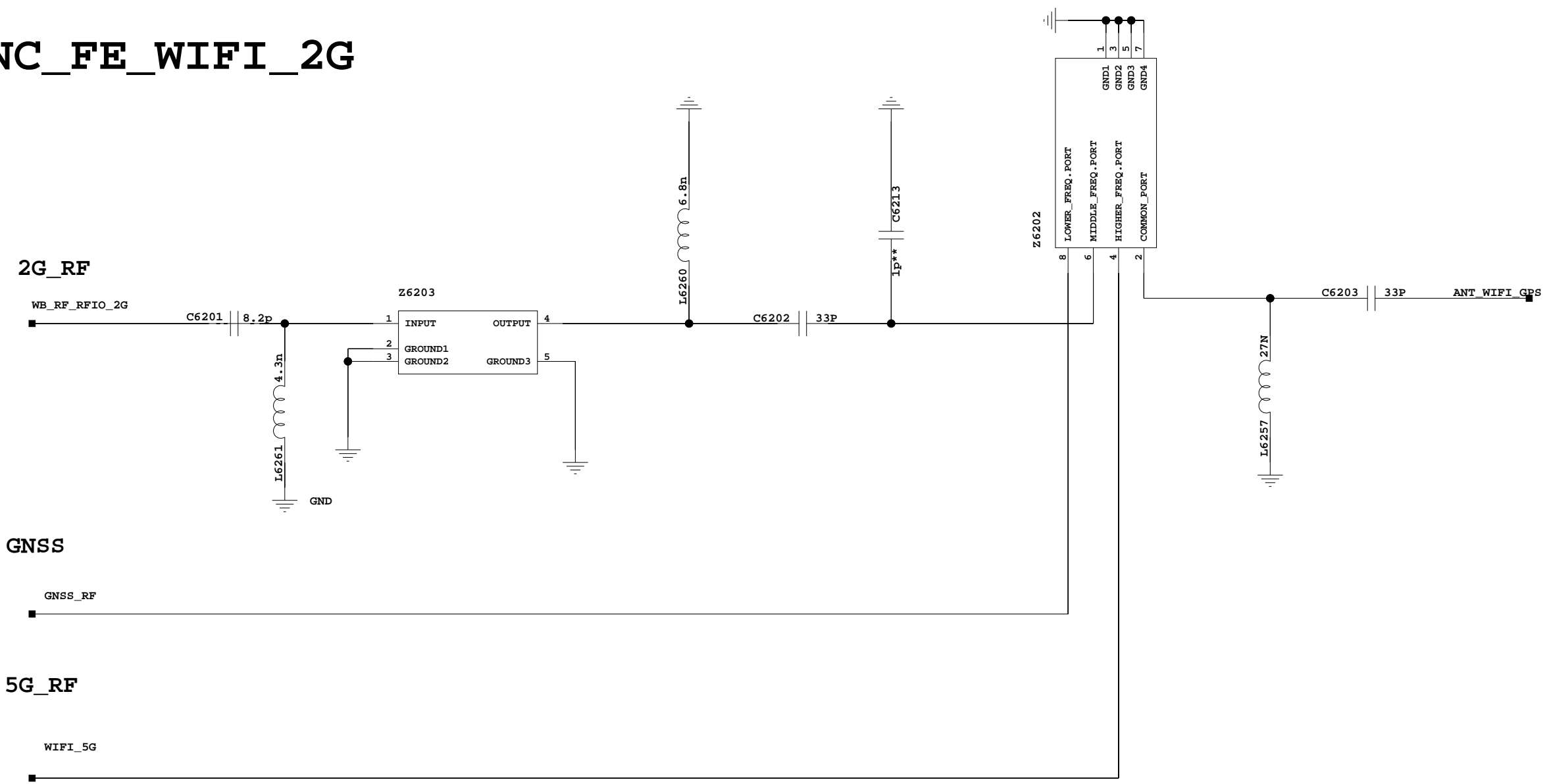


62 NC_FE_WIFI_2G

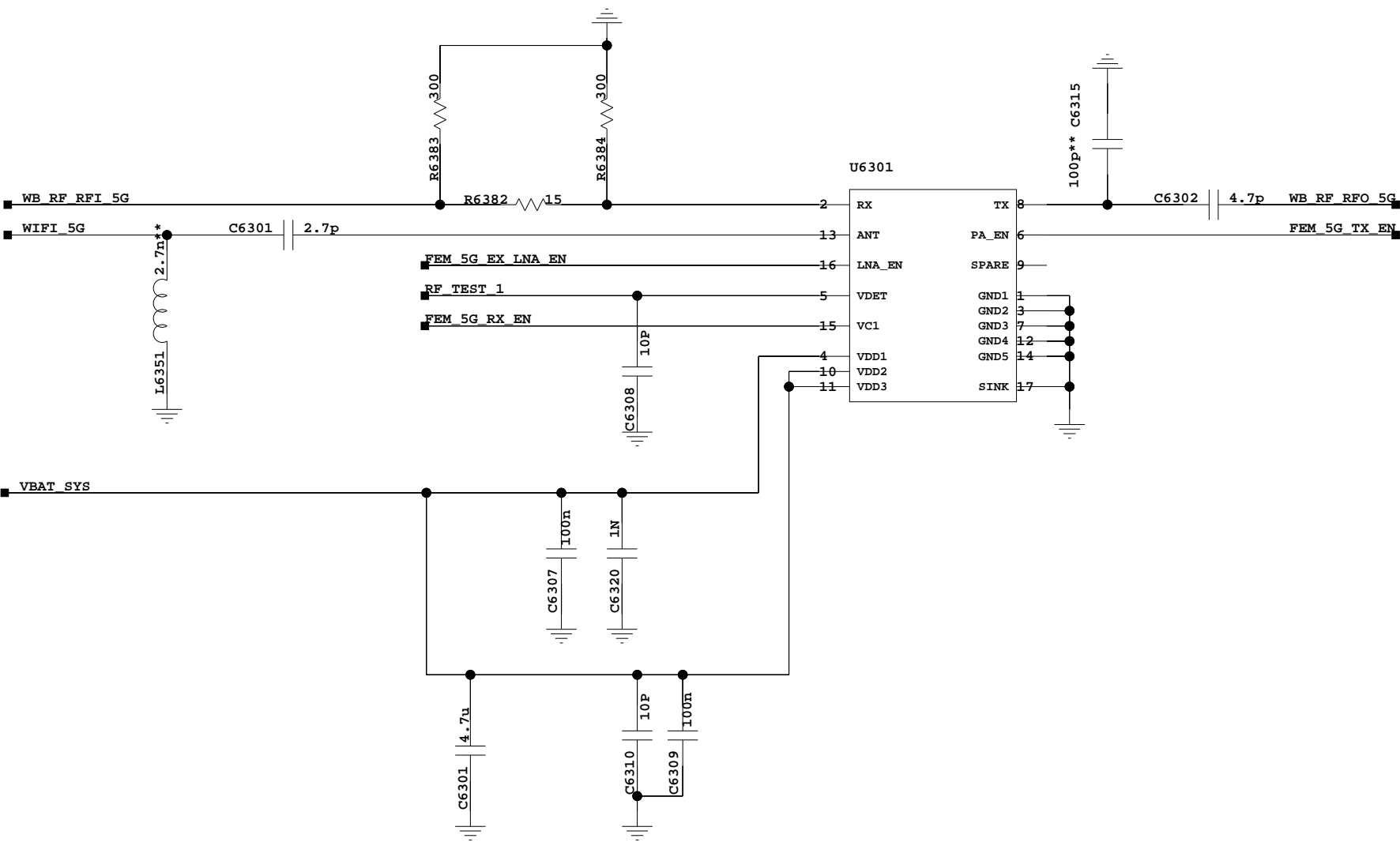
2G_RF

GNSS

5G_RF

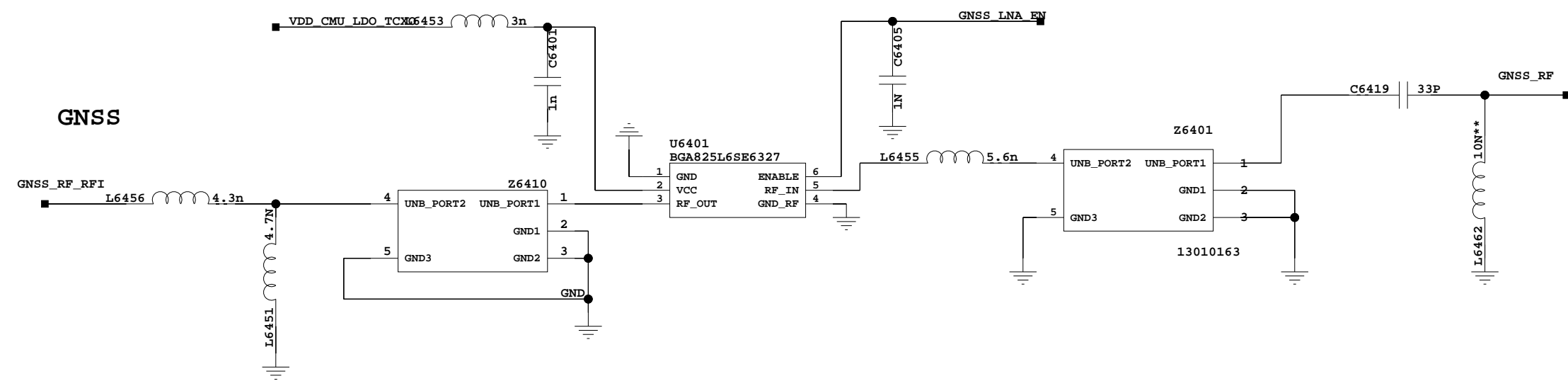


63 NC_FE_WIFI_5G



The type and specification of the components refer to the BOM

64 NC_GPS



65 RES

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

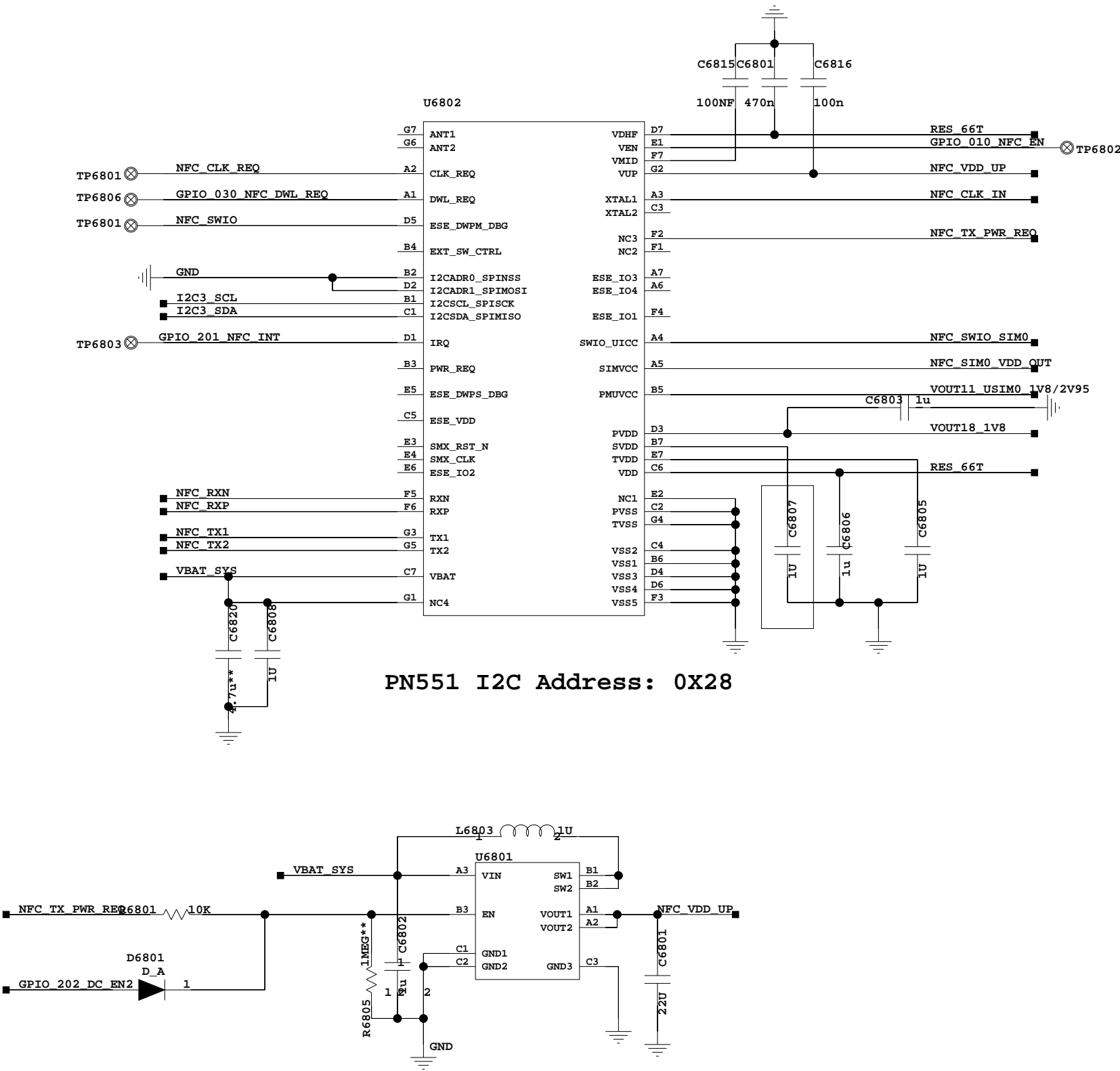
71 NFC Antena

1	2	3	4	5	6
A					
B					
C					
D					
1	2	3	4	5	6

67 RES

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

68. NFC_BB



PN551 I2C Address: 0X28

C6802 E/10uF 英 1uF 10/13

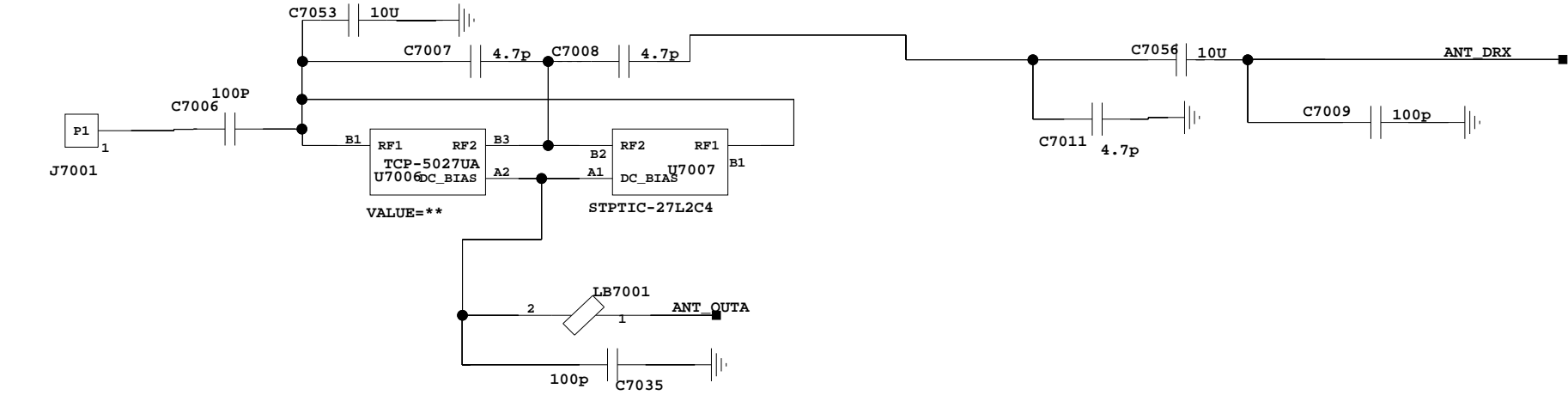
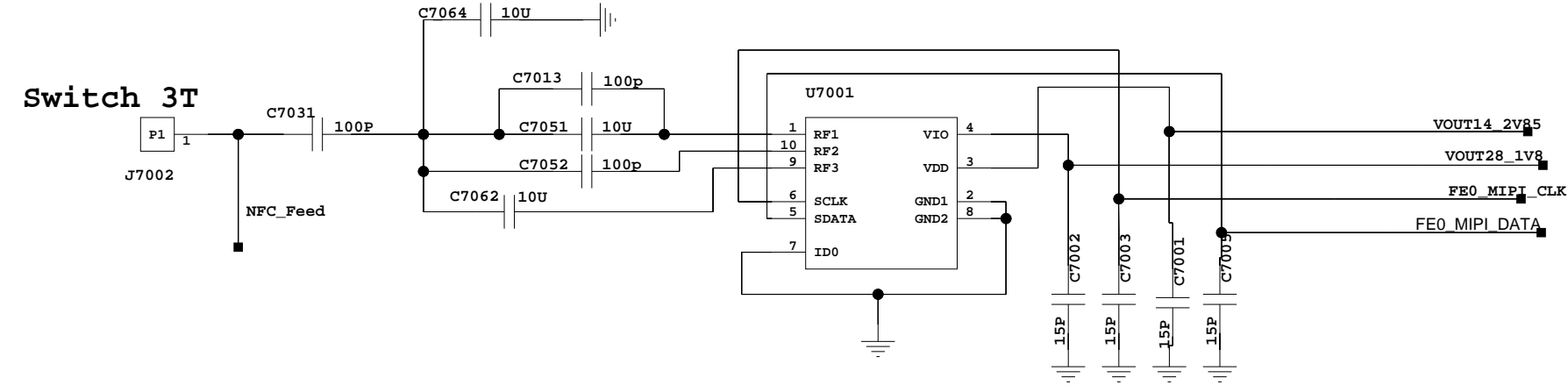
67 RES

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

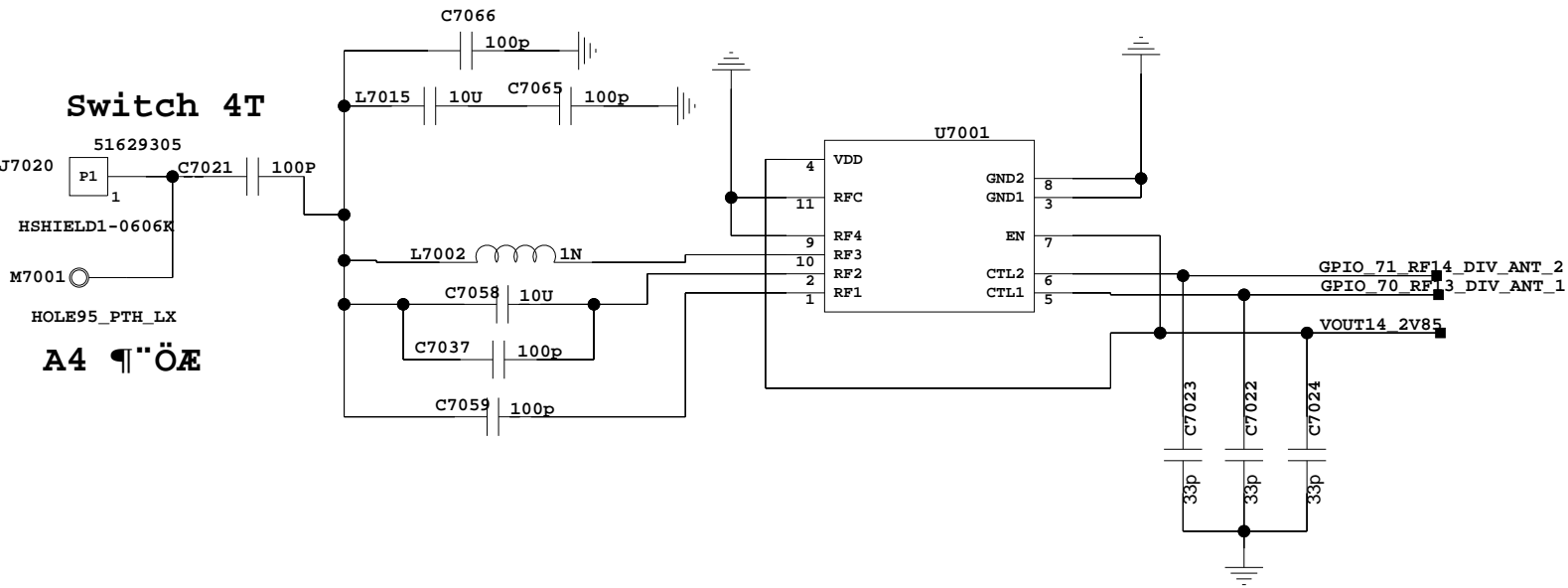
70.Antenna

Div Ant

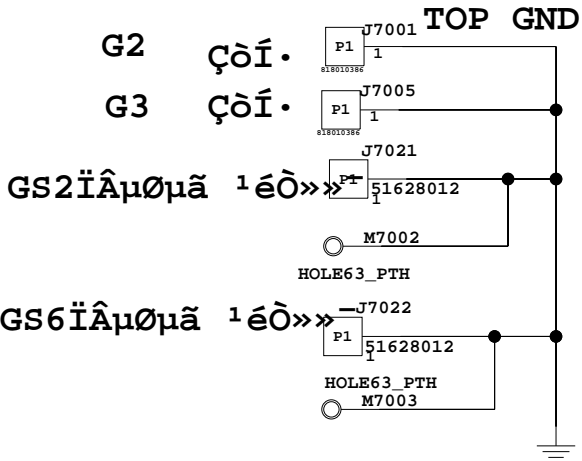
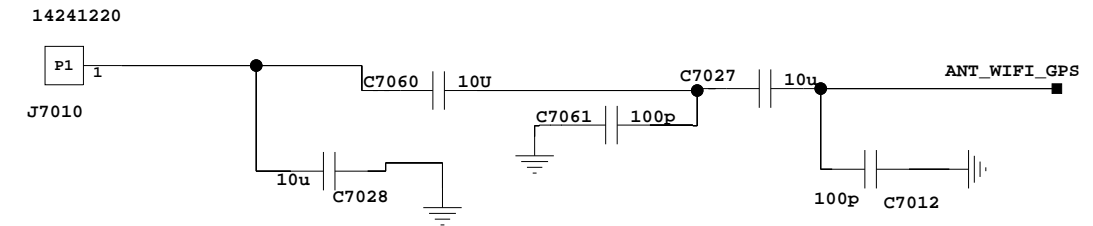
Switch 3T



Switch 4T



GPS WIFI 2G 5G



71 NFC Antena

